

## J.B.INSTITUTE OF ENGINEERING & TECHNOLOGY

### MANDATORY DISCLOSURE 2015-16

The following information is to be given in the Information Brochure besides being hosted on the Institution's official Website.

**“The information has been provided by the concerned institution and the onus of authenticity lies with the institution and not on AICTE.”**

#### I. NAME OF THE INSTITUTION: J.B.INSTITUTE OF ENGINEERING & TECHNOLOGY

➤ Address including telephone, Fax, e-mail.

Name of the Institution	<b>J.B.INSTITUTE OF ENGINEERING &amp; TECHNOLOGY</b>
	<i>BHASKAR NAGAR,</i>
Village	<i>YENKAPALLY, HYDERABAD – 500 075.</i>
Taluk	<i>MOINABAD (MANDAL)</i>
District	<i>RANGA REDDY</i>
Pin Code	<i>500 075</i>
State	<i>ANDHRA PRADESH, HYDERABAD – 500075</i>
Telephone No.	<i>08413-235127</i>
FaxNo.	<i>08413-235053</i>
E-Mail:	<a href="mailto:principal@jbiet.edu.in">principal@jbiet.edu.in</a>
Web site	<a href="http://www.jbiet.edu.in">www.jbiet.edu.in</a>

#### II. NAME & ADDRESS OF THE PRINCIPAL

➤ Address including telephone, Fax, E-mail.

Name	<i>Dr. Niraj Upadhyaya, Principal J.B.INSTITUTE OF ENGINEERING &amp; TECHNOLOGY, YENKAPALLY, MOINABAD MANDAL, RANGA REDDY DISTRICT, HYDERABAD – 75</i>
Telephone	<i>08413 – 235127</i>
Fax	<i>08413-235053</i>
E-Mail	<a href="mailto:principal@jbiet.edu.in">principal@jbiet.edu.in</a>

### III. NAME OF THE AFFILIATING UNIVERSITY:

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD. (JNTUH)  
KUKATPALLY, HYDERABAD – 500085.

### IV. Governance

#### Members of the Board of Governors (BOG) as per UGC

Sl. No	Name of the Member of BOG	Qualifications and Position in the Current Engagements	Position In the BoG	Nominated By
<b>Chairperson</b>				
1	Padmashri Prof .Kolakaluri Enoch	B.A.(Hons), Ph.D., Former Vice-Chancellor of Sri Venkateswara University, Tirupathi, Educationist	Chairman	JB Educational Society (JBES)
<b>Members of the Trust/Society/Management</b>				
2	Smt.J.Vasumathi Devi	President, J.B.Group of Educational Institutions, Educationist	Member	JBES
3	Sri.A.K.Chakrabarti	Former Director, DRDL	Member	JBES
4	Sri.J.V.Krishna Rao	MBA ( USA), Industrialist	Member	JBES
5	Sri .J.Vamshidhar Rao	MBA (USA), Industrialist	Member	JBES
<b>Two Faculty members of the Institution</b>				
6	Dr.C.Uday Kiran	B.Tech.,M.Tech., Ph.D., Prof. & HOD in Mechanical Engg.	Member	Chairman, BOG
7	Dr.D.N.Rao	B.Tech, M.E., Ph.D., MBA., Prof. & HOD in ECE.,	Member	Chairman, BOG
<b>Educationist or Industrialists</b>				
8	Prof. G.Sankara Rao	B.E. (Mech), M.Tech., MISTE, Former Principal of Engg. College, JNTUH & Former Registrar of JNTUH, Educationist	Member	Chairman, BOG
9	Prof. A.K Asthana,	B.E. (Civil), M.Tech., Ph.D., Former Principal of Engg. College, JNTUH, Educationist	Member	Chairman, BOG
<b>One nominee of the AICTE/UGC. Nomination requested can be accepted-attach the request letter (See ANNEX-1 &amp; 2 of Part-1)</b>				
10*	Prof. Keshari Lal Verma	Pt. Ravishankar Shukla University, Raipur former Chairman, Commission for Scientific & Technical Terminology & Director Central Hindi Directorate, MHRD, Government of India	Member	Nominated by UGC
11*	To be nominated by AICTE		Member	To be nominated by AICTE
<b>One nominee of the State Government. Nomination requested can be accepted-attach the request letter (See ANNEXURE-3 of Part-1)</b>				
12*	Principal, GPC	Principal, Government Polytechnic College, Masab Tank, Hyderabad, Educationist	Member	TS State Gov.
<b>One nominee of the University to which the Institution is affiliated. Nomination requested can be accepted-attach the request letter (See ANNEXURE-4 of Part-1)</b>				
13	Prof.A.Rama Krishna Prasad	M.Sc, M.Tech (ES), M.B.A., Ph.D., Director, UIIC, JNTUH, Educationist	Member	JNTUH
<b>Head of the Institution, Ex-officio</b>				
14	Dr. Niraj Upadhayaya	B.Tech, M.Tech, Ph.D, M.B.A.,LCSI,LACM	Member Secretary	Ex-officio

\* Requested to nominate by the UGC vide letter Ref. No. JBIET/Prin.UGC/2010-11 dated 06.11.2010

- **BRIEF BACK GROUND notes in respect of the above members of the Governing body are enclosed herewith.**

❖Members of Academic Advisory Body

i)	Shri.A.K.Chakrabarti, Former Director DRDL	- Advisor
ii)	Dr. Niraj Upadhayaya, Principal, JBIET	- Chairman
iii)	Prof.G.Sreenivasulu, HOD (CSE), JBIET	- Member
iv)	Dr. Shiva Prasad, HOD (EEE), JBIET	- Member
v)	Dr.D.N.Rao, HOD (ECE), JBIET.	- Member
vi)	Prof.S.Shrihari, HOD (CIVIL)	- Member
vii)	Dr. C.Udaya Kiran, HOD(MECH)	- Member
viii)	Dr. Rimpay Bishnoi, Assoc.Prof. (IT),JBIET	- Member
ix)	Dr.G.Kameshwar Rao, HOD(S&H), JBIET	- Member

❖Frequency of the Board Meetings and Academic Advisory Body

- 2 Times per year

\* Organizational chart and processes: --Copy enclosed.

❖Nature and Extent of involvement of faculty and students in academic affairs/  
Improvements:

*The strength and validity of this institution essentially depends on its ability to sustain itself on the bedrock of quality, excellence and relationships and governance.*

*To achieve this, we rely on our faculty and the staff, their abilities attitudes, morale, the professional ethos, and their unwavering commitment and involvement, their cooperation with the Institute administration between members of the Department and inter-departmental cooperation and synergizing their talents, efforts and services functioning as energetic nerve-centers of academic ferment and thereby ensuring, innovative inspiring and supportive academic environment for enabling the students, to pursue their educational, career and professional goals.*

*It is in the said context, and keeping in view the primary objective of extensive and effective involvement of the faculty, to carry out a wide range of student-centric and student friendly activities ,and programmes focusing on curricular, co-curricular, extracurricular activities, and finally aiming at the holistic personality development of students, number of committees have been constituted,involvingallthefacultymembersatallthelevels,whilealsoincludingthenon- teachingstaff,asco-optedmembersandinvolvingthestudentsparticipation.*

❖Mechanism/Norms & Procedure for democratic/good Governance

*It is at the threshold of this millennium, in the year1998, J.B.Institute of Engineering and Technology was sponsored and established by J.B. Educational Society, a very progressive, dynamic and visionary management, with excellent academic track record. This institution is recognized by All India Council for Technical Education approved by Government of Telangana and affiliated to Jawaharlal Nehru Technological University. The vision, Mission and the quality policy and the quality objectives are as indicated in the annexure enclosed herewith.*

*The Governance of the institution is strictly in accordance with the vision, mission, and Quality policy statements of this management and the norms standards, systems and practices laid down and various executive directions issued by AICTE, JNTU, Government of Telangana Council for Higher Education and such other related regulating Government agencies. As per the policies evolved and directions issued by the Governing body and the executive body and other management committees of this Institute, various institutional programmes and activities are carried-out, while complying with the rules, systems, procedures and practices as laid down by the management.*

*The Principal is responsible for the academic, general and financial administration besides the all-round development of the institution, discipline among all the faculty, staff and students, besides their welfare. The Principal is ably assisted by the HOD's, Joint Director (HR), Training & Placement Officer in charge of various functional units and the administrative staff besides the committed contribution of all the conveners and officers in charge of various committees constituted (list enclosed), for carrying out student centric and student friendly activities, placement and training, industry institution interaction, consultancy and R&D activities.*

*Administrative Manual is prepared while clearly defining and stipulating recruitment policies, service rules, leave rules administrative systems and practices including grievance redressal procedures etc. The Finance manual has been prepared and all the financial transactions are made, complying with the systems, norms, procedures and practices, laid down in the said manuals.*

*The performance appraisal of teaching staff including the feedback by the students, and the feedback of the students about the Governance of the institution are being administered, analyzed and the needed remedial measures are initiated. Centralization of the institutional activities is ordered while also delegating the academic administrative and financial powers, to tone up the functional efficiency, duly fixing and defining the duties, responsibilities answer ability and accountability of all concerned from the level of the Principal to the lowest level of employee, in the official hierarchy*

#### **❖ Student Feedback on Institutional Governance/faculty performance**

##### **Student feedback on Institutional Governance**

*All the outgoing final year students are supplied with a prescribed format,( at the time of receiving “ No Objection Certificate “) designed for obtaining the feedback on the Institutional Governance as approved and decided upon by the college Academic Committee (A Copy of the format is enclosed herewith).*

*The detailed summary and analysis of the Feedback of all the outgoing students is recorded and documented. In the meetings of the college Academic Committee Members and the Heads of the Departments, the deficiencies, in adequacies and short comings indicated about the institutional governance and suggestions made by the said outgoing students if any, will be earnestly discussed and necessary corrective measures will be evolved for effective implementation.*

##### **Student feedback on Faculty performance**

*Student Feedback on faculty performance is done once in a semester on a day preferably at the end of the semesters, on the day of last examination.*

*The students of a class attending on the particular day, fixed for administrating the feedback process, are asked to assemble in a room and are supplied with a prescribed format, Say format-I(as approved and decided by the college Academic Committee)to obtain students feedback on the performance of all the faculty in the respective subjects taught by them during semester under consideration.*

*The feedback form contains a certain number (10) of points of feedback and each such feedback point carries a certain maximum weight age (4) and thus each subject carries the score of 40.*

*This process will be administrated by a senior responsible staff (duly appointed by the Principal/HOD) other than the concerned subject teachers. The students shall not write their names or Roll numbers on the feedback form. The summary and analysis of the feedback of all the students present on the day will be recorded in a format, say Format-II. The percentage of score in each subject secured by a staff is computed by using the formula: Total Marks secured /No. of students x 40. The total score secured by all the teachers in all the subjects handled will be recorded in format-III.(A copy of the latest assessment of faculty by the students feed back in the prescribed format is enclosed herewith.*

*The details of the score of the students' feedback are communicated to the respective subject teachers, advising & counseling the faculty for improved performance by the Principal/ Head of the Department. In the event of a staff, securing less than the desired and stipulated percentage of score necessary remedial and corrective measures will be initiated, including arranging for Training intervention in pedagogical, subject updating and such other areas related to strengthening, teaching learning process.*

**❖ Grievance Redressal mechanism for faculty, staff and students**

*Grievance Redressal Committees have been constituted separately in respect of faculty, staff and students, with senior faculty / staff as Chairman & Members of the respective committee, as given below.*

<b>S.No</b>	<b>Name of the Cell</b>	<b>Officer Incharge</b>	<b>Member</b>
1	Cell to deal with Redressal of Faculty Grievances	Dr. Sumagna Patnaik, Professor & HOD (MCA) & Dean (SA)	Dr.B.V.Swarnalathamma, Prof., S&H
2	Cell to deal with Redressal of Grievances of Non Teaching Staff	Dr.G.Kameshwar Rao, HOD (S&H)	Mr. Ravi Kiran, AO
3	Cell to deal with Redressal of Grievances of Students	Dr.Tauhid Sultana, Prof. (ECE)	Mr.B.V.K.Murthy , Asst.Prof.(MECH)

*The aggrieved individual (Faculty/staff/student) shall indicate the details of his/her grievance in a prescribed format (designed for the purpose) and submit the same to the Principal, who in turn will refer the same to the Chairman of the concerned redressal committee.*

*The Grievance of the individual will be given fair and reasonable opportunity to be heard in detail before the Chairman and other members of the concerned Committee, in a peaceful and conciliatory environment depending upon the issue and if need be, appropriate evidence in the form of a material evidence or personal witness may be introduced by the aggrieved person.*

*The Chairman of the Committee, in consultation with the other member(s) will submit the report to the Principal, at the earliest possible time.*

*The Principal in turn, depending upon the nature, magnitude and jurisdiction of the issue, will arrange for the appropriate and early measure of redressal of grievance, under the direction of the management and the same will be communicated to the 'aggrieved person'. All the proceedings of the grievance mechanism will be suitably documented and recorded.*

## V. PROGRAMMES

❖Name of the Programmes approved by the AICTE

<b>S.No.</b>	<b>Name of the Programme</b>	<b>Sanction Intake</b>
<b>UG</b>	<b>B.Tech.</b>	
1	Electrical & Electronics Engineering	120
2	Mechanical Engineering	120
3	Electronics & Communication Engineering	120
4	Computer Science Engineering	120
5	Information Technology	60
6	Civil Engineering	120
7	Mining Engineering	60
8	Electronic & Computer Engineering	60
<b>PG</b>	<b>M.Tech.</b>	
1	Electrical Power Systems	18
2	Computer Science Engineering -	18
3	VLSI System Design	24
4	Software Engineering	18
5	CAD/CAM	18
6	Energy Systems	18
7	Structural Engineering	18
8	MCA(Master ofComputer Applications)	60
9	MBA	60

We are submitting proposals for renewal of Accreditation of the UG Programme in B.Tech (ECE) and B.Tech (EEE), Applied for NBA Accreditation Office Letter No: JBIET/Accrdtm/NBA/09, Dt.21.05.2009.

Application submitted for accreditation B.Tech (IT, SE, BME), M.Tech(EPS), M.Tech(CSE), MBA(OU).

❖ For each Programme the following details are to be given:

• Placement facilities

Name	Num ber of seats	Durat ion	Cutoff marks/rank for admission during the last three years					
			2012-13		2013-14		2014-15	
<b>B.Tech.</b>			<b>EAMCET</b>	<b>Management Quota</b>	<b>EAMCET Convenor Quota</b>	<b>Management Quota</b>	<b>EAMCET Convenor Quota</b>	<b>Management Quota</b>
EEE	120	4 yrs	40 marks out of a maximum of 160 marks in EAMCET	the basis of EAMCET Rank 50% of Aggregate orin Groupsof Intrmdt. Or Equilt. Exam. However for large leftover seats after final counselingby Convener, 50% marksneed not be insistedand a pass percentagemay be considered following merit order as per A.P.Govt. ordersin G.O.Rt.No.974, Higher Education (EC) Dept., dt.6.12.05. Admission process isinprogress.	40 marks out of a maximum of 160 marks in EAMCET	the basis of EAMCET Rank 50% of Aggregate orin Groupsof Intrmdt. Or Equilt. Exam. However for large leftover seats after final counselingby Convener, 50% marksneed not be insistedand a pass percentagemay be considered following merit order as per A.P.Govt. ordersin G.O.Rt.No.974, Higher Education (EC) Dept., dt.6.12.05. Admission process isinprogress.	40 marks out of a maximum of 160 marks in EAMCET	the basis of EAMCET Rank 50% of Aggregate orin Groups of Intrmdt.Or Equilt. Exam. However for large leftover seats after final counselingby Convener, 50% marksneed not be insistedand a pass percentage may be considered followingmerit order as per A.P.Govt. ordersin G.O.Rt.No.974, Higher Education (EC) Dept., dt.6.12.05. Admission process isinprogress.
MECH	120	4 Yrs						
ECE	120	4 Yrs						
CSE	120	4 Yrs						
IT	120	4 Yrs						
CIVIL	60	4 yrs						
MINING	60	4 yrs						
ECM	60	4 yrs						
<b>M.Tech.</b>			<b>PFConvenor Quota (70% of intake)</b>	<b>Management Quota (30% of intake)</b>				
Electrical Power Systems	18	2 Yrs	13	05	TheLatestAICTEwebsitenotification.			
ComputerScience Engineering	18	2 yrs	13	05				
VLSI (System Design)	18	2 yrs	13	05				
Software Engineering	18	2 yrs	13	05				
CAD/CAM	18	2 yrs	13	05				
MCA	60	3 Yrs	42	18				
MBA(JNTUH)	60	2 yrs	42	18				
MBA(OU)	120	2 yrs	84	36				

**Fee** : **Rs.48,900/- per annum for a student allotted by EAMCET Coordinator, and also student admitted under Management quota.**

We are in touch with Organisations like M/s.Infosys, Cognizant Technologies, Wipro Technologies, Satyam Computers, L&T Infotech, M/s Virtusa, GTL Lt , Computer Science Corporation, Cognizant Technology, and other reputed Organisations and they have included our college in their list of recruitments

Apart from Placement Activities, we are preparing the Students on Soft Skills also. We have an MOU with M/s.Globarena, who have provided the Software for communication skills. We have CD's for Aptitude Tests, Psychometric Tests etc., so that the Students can get trained on them. We have also established a lab for Aptitude and Technical skills learning by the Students so that they can fare well in the Interviews. Also we are conducting in house soft skills training programmes as par to their curriculum.

**ORIENTATION TOWARDS INTERVIEWS:** We are organizing workshops by some of the well known Institutions like T.I.M.E and Visu International to provide an understanding to the Students on how Group discussions and Interviews take place. Also we are organizing Interactive sessions with H.R Executives of some top Companies like M/s.POLARIS LABS LIMITED, Wipro Technologies etc. Recently, we organized a Seminar on prospects in IT Industry by M/S.CMC Education Centre. We are also giving emphasis on Entrepreneurship Development and we arranged Lectures on this subject and we are also interacting with JNTU Entrepreneur Cell for this.

**INDUSTRIAL VISITS:** We are in touch with Industries like BHEL, ARM, ECIL, RANEENGINE VALVES, VIJAI ELECTRICALS, APSEB etc., for Industrial visits for the Students, so that they will have practical Orientation. We are also arranging industrial tours along with Departments.

❖ Campus placement in last three years with minimum salary, maximum salary and average salary.

<b>J B INSTITUTE OF ENGINEERING &amp; TECHNOLOGY</b>					
<b>FOR 2009 - 2013 BATCH ( 2013 PASS OUTS )</b>					
<b>S.No</b>	<b>Year of Passing</b>	<b>Dept.</b>	<b>Name of the Company</b>	<b>No.of Students Selected</b>	<b>Total No.of Selected</b>
1	2012-13	CSE	IGATE	6	37
	2012-13	CSE	INFOTECH	4	
	2012-13	CSE	CAPGEMINI	2	
	2012-13	CSE	DELL	4	
	2012-13	CSE	ECLATURE	1	
	2012-13	CSE	SYBERPLACE	7	
	2012-13	CSE	DCS LABS	13	
2	2012-13	IT	IGATE	10	21
	2012-13	IT	DELL	1	
	2012-13	IT	ECLATURE	1	
	2012-13	IT	KANTAR OPERATIONS	1	
	2012-13	IT	SYBERPLACE	5	
	2012-13	IT	DCS LABS	3	
3	2012-13	ECE	IGATE	6	23



	2012-13	ECE	INFOTECH	3	
	2012-13	ECE	SYBERPLACE	9	
	2012-13	ECE	DCS LABS	5	
4	2012-13	EEE	IGATE	8	24
	2012-13	EEE	INFOTECH	5	
	2012-13	EEE	CAPGEMINI	2	
	2012-13	EEE	KANTAR OPERATIONS	1	
	2012-13	EEE	SYBERPLACE	8	
5	2012-13	MECH	IGATE	1	7
	2012-13	MECH	MOLD-TEK	1	
	2012-13	MECH	COMMLAB INDIA	1	
	2012-13	MECH	HYUNDAI	4	
6	2012-13	CIVIL	MOLD-TEK	1	1
7	2012-13	BME	HEALTH MANTRA	1	1
	Total On through ON Campus selection			(A)	114
	Students selected through OFF Campus Selections			(B)	108
	Total Number of Students Placed			(A + B)	222

**J B INSTITUTE OF ENGINEERING & TECHNOLOGY**

**FOR 2010 - 2014 BATCH ( 2014 PASS OUTS )**

S.No	Year of Passing	Dept.	Name of the Company	No.of Students Selected	Total No.of Selected
1	2013-14	CSE	INFOSYS	12	43
	2013-14	CSE	IBM	1	
	2013-14	CSE	OSMOSYS	2	
	2013-14	CSE	INFOTECH	3	
	2013-14	CSE	SYNTEL	20	
	2013-14	CSE	TRIBRO	2	
	2013-14	CSE	SUTHERLAND	2	
	2013-14	CSE	OG ANALYSIS	1	
2	2013-14	IT	INFOSYS	6	27
	2013-14	IT	PERSISTANT SYSTEMS	1	
	2013-14	IT	IBM	1	
	2013-14	IT	KTREE	2	
	2013-14	IT	OSMOSYS	4	
	2013-14	IT	SYNTEL	6	
	2013-14	IT	IPRIMED	3	
	2013-14	IT	RISE	1	
	2013-14	IT	KANTAR OPERATIONS	1	
	2013-14	IT	TRIBRO	2	

3	2013-14	ECE	INFOSYS	7	26
	2013-14	ECE	OSMOSYS	1	
	2013-14	ECE	INFOTECH	2	
	2013-14	ECE	SYNTEL	11	
	2013-14	ECE	IPRIMED	5	
4	2013-14	EEE	INFOSYS	3	36
	2013-14	EEE	IBM	2	
	2013-14	EEE	INFOTECH	3	
	2013-14	EEE	SYNTEL	19	
	2013-14	EEE	IPRIMED	7	
	2013-14	EEE	TRIBRO	2	
5	2013-14	MECH	INFOSYS	4	12
	2013-14	MECH	HYUNDAI	4	
	2013-14	MECH	INDIAN NAVY	1	
	2013-14	MECH	OG ANALYSIS	3	
6	2013-14	CIVIL	INFOSYS	1	2
	2013-14	CIVIL	IBM	1	
7	2013-14	MCA	MPHASIS	2	11
	2013-14	MCA	AT&T	3	
	2013-14	MCA	SYNTEL	4	
	2013-14	MCA	RISE	2	
8	2013-14	M.Tech	AT&T	2	2
Total On through ON Campus selection (A)					159
Students selected through OFF Campus Selections (B)					123
Total Number of Students Placed (A + B)					282
<b>J B INSTITUTE OF ENGINEERING &amp; TECHNOLOGY</b>					
<b>FOR 2011 - 2015 BATCH ( 2015 PASS OUTS )</b>					
S.No	Year of Passing	Dept.	Name of the Company	No.of Students Selected	Total No.of Selected
1	2014-15	CSE	INFOSYS	16	30
	2014-15	CSE	MPHASIS	3	
	2014-15	CSE	OSMOSYS	2	
	2014-15	CSE	MINDTREE	3	
	2014-15	CSE	EXCERS	1	
	2014-15	CSE	POLARIS	1	
	2014-15	CSE	KTREE	1	
	2014-15	CSE	SYNTEL	3	
2	2014-15	IT	INFOSYS	11	36
	2014-15	IT	MPHASIS	1	
	2014-15	IT	PERSISTENT SYSTEMS	1	
	2014-15	IT	OSMOSYS	2	
	2014-15	IT	MINDTREE	5	

	2014-15	IT	AMAZON	3	
	2014-15	IT	EXCERS	2	
	2014-15	IT	KTREE	2	
	2014-15	IT	3X3 CONNECT	5	
	2014-15	IT	CALIBER TECHNOLOGIES	1	
	2014-15	IT	KANTAR OPERATIONS	1	
	2014-15	IT	E TOUCH SYSTEMS INDIA PVT LTD	1	
	2014-15	IT	SYNTEL	1	
3	2014-15	ECE	INFOSYS	13	24
	2014-15	ECE	OSMOSYS	2	
	2014-15	ECE	AMAZON	3	
	2014-15	ECE	ICICI	1	
	2014-15	ECE	POLARIS	2	
	2014-15	ECE	HYUNDAI	2	
	2014-15	ECE	E TOUCH SYSTEMS INDIA PVT LTD	1	
4	2014-15	EEE	INFOSYS	5	14
	2014-15	EEE	MINDTREE	1	
	2014-15	EEE	ANNEX GLASS INDUSTRIES	2	
	2014-15	EEE	KANTAR OPERATIONS	2	
	2014-15	EEE	E TOUCH SYSTEMS INDIA PVT LTD	2	
	2014-15	EEE	SYNTEL	2	
5	2014-15	MECH	INFOSYS	6	14
	2014-15	MECH	AMAZON	1	
	2014-15	MECH	CYIENT	3	
	2014-15	MECH	ANNEX GLASS INDUSTRIES	4	
6	2014-15	CIVIL	INFOSYS	1	3
	2014-15	CIVIL	ICICI	1	
	2014-15	CIVIL	E TOUCH SYSTEMS INDIA PVT LTD	1	
7	2014-15	BME	IBM	1	5
	2014-15	BME	AMAZON	2	
	2014-15	BME	THYROCARE	2	
8	2014-15	MCA	WIPRO	2	4
	2014-15	MCA	SYNTEL	1	
	2014-15	MCA	AT & T	1	
	Total On through ON Campus selection			(A)	130
	Students selected through OFF Campus Selections			(B)	37
	Total Number of Students Placed			(A +B )	167

**\*Highest Salary : Rs.3.58 lakhs per annum**

**\*Lowest Salary : Rs.1.8 lakhs per annum**

**\*Average Salary : Rs.2.4 lakhs per annum**

❖ Name and duration of programme(s) having affiliation/collaboration with Foreign University(s)/Institution(s) and being run in the same Campus along with status of their AICTE approval. If there is foreign collaboration, give the following details:

Details of the Foreign Institution/University -- **Not Applicable--**

- Name of the University/Institution ---
- Address \* Website ---- **Not Applicable--**
- Is the Institution/University Accredited in its Home Country
- Ranking of the Institution/University in the Home Country
- Whether the degree offered is equivalent to an Indian Degree?

If yes, the name of the agency which has approved equivalence.

If no, implications for students in terms of pursuit of higher studies in India and abroad and job both within and outside the country.

- Nature of Collaboration\* Conditions of Collaboration
- Complete details of payment a student has to make to get the full benefit of collaboration

2. For each affiliated Programme give the following:

- a. Programme Focus :
- b. Number of seats :
- c. Admission Procedure :
- d. Fee :
- e. Placement Facility :
- f. Placement Records for last three years with minimum salary, maximum salary and average salary

**-Not Applicable**

❖ Whether the Collaborative Programme is approved by AICTE, If not whether the Domestic/Foreign Institution has applied to AICTE for approvals required under notification no.37-3/Legal/2005, dated 16<sup>th</sup> May, 2005 - **Not Applicable**

## VI. FACULTY DETAILS:

### ❖ Branch wise list faculty members:

S.No.	Branch	Permanent Faculty	Adjunct Faculty*	Student Ratio 1:15
1	Electrical & Electronics Engineering	24	52 *	1:15
2	Mechanical Engineering	24		1:15
3	Electronics & Communication Engineering	24		1:15
4	Computer Science Engineering	24		1:15
5	Information Technology	12		1:15
6	Civil Engineering	24		1:15
7	Mining Engineering	12		1:15
8	Electronic & Computer Engineering	12		1:15
9	Electrical Power Systems	3		1:12
10	Computer Science Engineering -	4		1:12
11	VLSI System Design	3		1:12
12	Software Engineering	3		1:12
13	CAD/CAM	3		1:12
14	Structural Engineering	3		1:12
15	Energy Systems	3		1:12
16	MCA	15		1:15
17	MBA	20		1:15

**@:Staff of S&H Department are not included in the student ratio.**

*\*Staff of S&H(supporting department) 52 Nos*

**VII. PROFILE OF PRINCIPAL WITH QUALIFICATIONS, TOTAL EXPERIENCE,AGE AND DURATION OF EMPLOYMENT AT THE INSTITUTE CONCERNED :**

**1. Name of thePrincipal** : Dr.Niraj Upadhayaya

**2. Date of Birth** : September 13<sup>th</sup> 1966

**3. Education** : BE,ME, MS., MBA,Ph.D.

- i. **Doctor of Philosophy(Ph.D.)** from University of the West of England, Bristol, UK, November 2005, Specialization in Modification in the Memory Management of Clustered Supercomputer).
- ii. **MS** from India in 2000 Specialization in Software Systems from Birla Institute of Technology and Science(BITS), Pilani, Rajasthan.
- iii. **Bachelor of Engineering(B.E.)** from Gorakhpur University, UP, India in June 1987. Specialization in Electronics.

**4. Technical & Management Skills :**

Acquired the following Skills:

- i. Computer Science ( AI & ANN)
- ii. Parallel Computing
- iii. Embedded Systems
- iv. Broadcasting Engineering
- vi. Electronics Engineering
- vii. Software Systems
- viii. Financial Management
- ix. Time & Stress Management
- x. Skills such as Presentation, Coaching, Negotiation, Interviewing and Counseling

**5. Courses Attended and Conducted:**

- i. Research Methodology
- ii. CUDA Systems
- iii. Quality Assurance and Management
- iv. Linux Programming
- v. Research Software Tools
- vi. Problem Solving
- vii. Team Building
- viii. Research areas in Hard
- ix. Android Programming
- x. Windows Mobile Programming

**6. Professional Summary :**

- i. More than 27 years of experience as a Principal, Dean, Senior Consultant, DDG ( Govt of India) in Computer Science, IT, Training & Human Resources Development, as on to-date.
- ii. Strong exposure in the Competence Assessment and Accreditation of Programmes, Institutions, Supervisors, Technicians and Operators based on the Staff Skills Profile System (SSPS) identifying the Skill Areas and Skill Activities at various levels.
- iii. Excellent management, communication and interpersonal skills, accustomed to work in a team environment with tight schedules and capable of working efficiently under pressure, managing multiple projects and cross training sub-ordinates in other Functional areas.
- iii. Rich and varied experience in the following areas:
  - a) Managing the Technical and Management Training Institutes, Recruitment Services, Planning and Developing the Curriculum, and Controlling the Finance and Budgets.
  - b) Teaching and Training in various Electronics, Telecommunications, IT and management modules in various degree courses.
  - c) Planning and Developing the Multi Skills Training after identifying the gaps in the skills required for Company Supervisors, Technicians and Operators.
  - d) Very good experience in using the computer and developing the Projects and Presentation materials by integrating the Software such as Windows, Word, Excel & PowerPoint and Internet for different applications.

## **7. Work Experience :**

- i. Principal, J.B. Institute of Engineering & Technology, Moinabad, Hyderabad, AP, India, from December 2013 Till Date.
- ii. Vice Principal, JBIET from Dec 2010 to Nov. 2013.
- iii. Principal Sri K.S. Raju Institute of Science & Technology, from April 2010 to Nov. 2010
- iv. Professor & HOD (CSE) JBIET from February 2008 to March 2010
- v. VL & RA at UWE Bristol, UK from February 2002 to January 2006
- vi. IBS 87 batch Min of I&B from July 1989 to January 2002

## **8. Research Papers Published:**

1. David, S. K., Upadhayaya, N., Mk, S., & Usmani, A. M. (2010). Knowledge Discovery Technique for Web-Based Diabetes Educational System. *Journal of Health & Medical Informatics*, 01(01), 1–4. doi:10.4172/2157-7420.1000102
2. Gafur, M. A., Upadhayaya, N., & Sattar, M. A. (2010). ENHANCING THE PERFORMANCE OF ADHOC NETWORKS USING NEW ADAPTIVELY VARYING BACKOFF TECHNIQUE. *International Journal of Engineering Science*, 2(12), 7078–7086.
3. Gafur, M. A., Upadhayaya, N., & Sattar, S. A. (2011). ACHIEVING ENHANCED THROUGHPUT IN MOBILE ADHOC NETWORK USING COLLISION AWARE MAC PROTOCOL. *International Journal of Ad Hoc, Sensor & Ubiquitous Computing*, 2(1), 7–18.

4. Karthikeyan, S. (n.d.). A NOVEL APPROACH FOR ENERGY EFFICIENT HIERARCHY BASED ROUTING IN WIRELESS SENSOR NETWORKS. *Energy*.
5. M Abdul Gafur, Niraj Upadhayaya, S. A. S. (2011). An Alternative Value Added Fairness Index for Mobile Adhoc Network., 8(3).
6. Mohammed, A., Kavuri, R., & Upadhyaya, N. (2012). Fault tolerance: case study. *Proceedings of the Second ...*. Retrieved from <http://dl.acm.org/citation.cfm?id=2393240>
7. Mohammed, A., & Updhyaya, N. (n.d.). Need of Programming Models, tools and algorithms for new generation HPC ( EXASCALE System ).
8. Upadhayaya Niraj, K. Vijaya, G. Sreenivasulu: Rule based HPC Cluster Controller using PARULEL, First National Conference on Parallel Computing, Ludhiana 2006.
9. Rao, K. V. R., Sreenivasulu, G., & Upadhayaya, N. (2010). A SURVEY OF RECENT TRENDS IN IMPROVEMENT OF MULTI CORE PROCESSORS PERFORMANCE. *International J. of Engg. Research & Indu. Appls. (IJERIA)*, 3(II), 237–258.
10. Rao, K. V. R., & Upadhayaya, N. (2010). THE MULTI-CORE BOTTLENECK. *International J. of Multidiscipl. Research & Advcs. in Engg. (IJMRAE)*, 2(II), 149–159.
11. Shesagiri, T., Upadahayaya, N., & Kumari, G. (2012). An audiovisual corpus for audio perception and automatic Audio recognition (L). *IJECCE*, 3(4), 657–660. Retrieved from <http://www.ijecce.org/issue/243/show/publication>
12. Shesagiri, T., Upadhayaya, N., & Vijayakumari, G. (2012). End-to-End Text Recognition with Convolutional Neural Networks. *Science And Technology*, 1(2), 132–136.
13. Upadhayaya, N. (2003). Clustering and Grid Computing: Age of Crisis. *CRES*, 1(June 2003).
14. Upadhayaya, N. (2013). Affect of Parallel Computing on Multicore Processors. In *CCSIT 2013* (Vol. 2, pp. 172–189). Bangalore: Springer.
15. Upadhayaya, N., & Sreenivasulu, G. (2010). PULSE COUPLED NEURAL NETWORK BASED ANALYSIS TO FIND CORRELATION BETWEEN BRAINWAVE AND BEHAVIOURAL PATTERN. *International Journal of Multi-disciplinary. Research & Advances. in Engineering. (IJMRAE)*, 2(I), 7074–7074.
16. Upadhayaya, Niraj, V. K. (2012). Brain Controlled Artificial Legs. *International Journal of Computer Engineering & Technology (IJCET)*, 3(3), 104–111.
17. Updhyaya, Niraj, A. G. (2014). An Efficient Approach For Local Repairing In Mobile Adhoc Network. *J.B. Research Review*, 3(2), 34–40.
18. Vikram, K., Goverdhan, A., Upadhayaya, N., & Irshad, S. (2011). A Suggestive Evaluation of System Test Cases in OO Systems Through Carving and Replaying Differential Unit Test Cases : A Metric Context. In *International Conference on Technology and Innovation (ICTI 2011)* (pp. 188–196). Chennai, India.
19. Vikram, K., & Upadhayaya, N. (2011). Digital Image Watermarking: Scalar Wavelets versus Multiwavelets. *International Conference on Computer Engineering and Systems*, 2(3), 150–159. Retrieved from <http://ieeexplore.ieee.org/lpdocs/epic03/wrapper.htm?arnumber=4115528>
20. Vikram, K., Upadhayaya, N., Govardhan, A., & Ahamad, S. I. (2011). Spatial Database in Image Retrieval System. In N. Ali (Ed.), *International Conference on Technology & Management ICTM 2011* (pp. 423–427). Hyderabad. India.

21. Vikram, K., Upashyaya, N., Roshan, K., & Govardhan, A. (n.d.). Image Edge Detection. *Image (Rochester, N.Y.)*, (1), 216–220.
22. Upadhayaya Niraj, Vikram Karimella, (2011). Data Mining Tools and Techniques : a review. *Computer Engineering*, 2(8), 31–39.
23. Upadhayaya, Niraj. A CROSS LAYER INTERACTIVE PROTOCOL MECHANISM FOR MANET WITH A REALISTIC RESPONSE TO NETWORK STIMULATION. Vol 9, No 18 (2013): European Scientific Journal (ESJ) June Edition
24. Rajsekhar K., Upadhayaya Niraj . Communication Security in Real Time Advanced Inexpensive Networks., ICDCCOM 2014 Ranchi (IEEEExplore), 12-13 Sept 2014, pages 1 -6 .
25. Rao KVR, Upadhayaya Niraj. Revision of Amdahl’s law for COTS cluster for a highly parallel task, Second International Conference on Advances in Computer Science and Application-CSA2013 (IEEE Conference), Lucknow India. 21<sup>st</sup> and 22<sup>nd</sup> July 2013.
26. Upadhayaya Niraj, madhu g, Govardhan A. and Rajnikanth T.V. A Scalable Formalism for Handling Uncertainty in Semantic web, TECHNIA International Journal of Computer Science and Communication Technologies, Vol 5 No.2 Jan 203 Pp No. 419-424.
27. Upadhayaya Niraj and V.R. Rao Kokimalla. Affect of Parallel computing on Multi Core Processors, Jan Zizka (Eds) CCSIT, SIPP,ASIC, PDCTA-2013, pp 41-54
28. Karimella Vikram, K Venkatesh Reddy, Dr. A. Goverdhan, Dr. Niraj Upadyaya. Colour Image Segmentation, International Journal of Electronics and Communication Technology, Vol 5 Issue 1, Jan-March 2014.
29. Upadhayaya Niraj, Sreenivasulu G.: ANN Based TRP prediction for new programmes on Indian Television Network Doordarshan, BES Review 2014, PP 46-54
30. Upadhayaya Niraj, Sreenivasulu G.: Using Brainwave and ANN for Behavioral Mapping: IJMRAE (International Journal of Multidisciplinary Research and Advances in Engineering) ISSN: 0975-7074: Vol 6, No.1 April 2014

## VIII. Systems FEE

### ❖ Details of fee, as approved by State fee Committee, for the Institution.

- *Rs.48,900/- per annum for a candidate admitted by Convenor(EAMCET) Rs.48,900/-per annum for a candidate admitted under Management quota*

### ❖ Time schedule for payment of fee for the entire programme.

*At the time of Admission into first year, fee shall be paid at the time of admission at TSEAMCET Counseling camp.*

*During the subsequent year’s i.e.2<sup>nd</sup>, 3<sup>rd</sup>&4<sup>th</sup> fee is collected in lump-sum at the very beginning of the academic session. However in deserving cases payment of fee in 2 installments per year is permitted.*

### ❖ Number of scholarships offered by the institute, duration and amount

*Waiver of fee, for those who apply on grounds of poverty*

### ❖ Criteria for fee waivers / scholarship.

*For economically backward and academically bright students, fee waivers are granted.*

### ❖ Estimated cost of boarding and Lodging in Hostels.

*The cost varies between Rs.3800/-to Rs.4000/-per month.*



**IX. ADMISSION:**

**❖Number of seats sanctioned with the year of approval.**

No.	Name	Number of seats	Duration	Approval No
<b>UG</b>				
1	EEE	120	4year	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
2	MECH	120	4Year	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
3	ECE	120	4Year	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
4	CSE	120	4Year	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
5	IT	60	4Year	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
6	Civil	120	4year	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
7	Mining Engg.	60	4	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
8	ECM	60	4	F.No:South-Central/1-2454679437/2015/ EOA, 07/04/2015
<b>PG</b>				
1	EPS	18	2Year	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
2	CSE	18	2year	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
3	VLSI System Design Course	24	2years	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
4	Software Engineering	18	2years	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
5	CAD/CAM	18	2year	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
6	Structural Engineering	18	2Year	
7	Energy Systems	18	2years	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
6	MCA	60	3Year	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013
7	MBA	60	2year	F.No:South-Central/1-1464949462/2013/ EOA, 19/3/2013

**❖Number of students admitted under various categories each year in the last three years:**

**(a)Pertaining to UG Programme**

Course/Year	Scheduled Caste		Scheduled Tribal		Backward class		Other's	Total Enrolment
	Male	Female	Male	Female	Male	Female		
B.Tech.I/IV 2015-2016 Year	60	14	30	05	200	23	200	532
B.Tech.I/IV 2014-2015 Year	150	41	55	07	220	11	236	720
B.Tech.I/IV 2013-2014 year	45	15	20	03	106	38	343	570
B.TechI/IV	29	20	13	04	96	39	219	420
2012-2013								
<b>TOTAL</b>	<b>284</b>	<b>90</b>	<b>118</b>	<b>19</b>	<b>622</b>	<b>111</b>	<b>998</b>	<b>2242</b>

**❖Number of students admitted under various categories each year in the last three years:**

**(a)Pertaining to PG Programme**

Course/Year	Scheduled Caste		Scheduled Tribal		Backward class		Other's	Total Enrolment
	Male	Female	Male	Female	Male	Female		
M.Tech.I Year 2015-2016Year	3	1	2	2	21	10	51	90
M.Tech.IIYear 2014-2015Year	5	2	3	2	24	13	41	90
<b>TOTAL</b>	<b>8</b>	<b>3</b>	<b>5</b>	<b>17</b>	<b>45</b>	<b>23</b>	<b>92</b>	<b>180</b>

❖ **Number of applications received during last two years for admission under Management Quota and number admitted.**

**X. ADMISSIONPROCEDURE**

*The entire admission–allotment process including receiving applications from the aspiring candidates is undertaken by the Convener TSEAMCET in respect of B.Tech Programme, Convenor (TSPGCet) in respect of M.Tech Programmes and Convener (TSICet) in respect of MBA and MCA Programmes and the details of applications received etc are not available at these office.*

*The Admission procedure in respect of seats under convener quota as well as management quota (NRI) is in accordance with the procedures laid down by TSCHE. - A copy of Rules, conditions and procedures is enclosed herewith.*

*Starting the academic session is as per the declaration of commencement of the academic year by the affiliating University.*

*In respect of cancellation of a seat during the period of the counseling, till the last date of last phase of counseling–cancellation fee is levied on the students at the rate of Rs.3,500/-In respect of all the candidates other than SC,ST candidates and in respect of SC,ST students the cancellation fee is Rs.1,500/-and the remaining fee paid will be refunded by the Convener in the respect Camps of EAMCET, PG Cet and I-Cet Admissions.*

❖ **Mention the admission test being followed, name and address of the Test Agency and its URL (website).**

Convener, TSEAMCET Website: <http://tseamcet.dte.gov.in>  
 (Under Govt.of Telangana)  
 Chairman of TSEAMCET- Admission & TSEAMCET-Examinations is the  
 Chairman of Telangana State Council for Higher Education.

❖ **Number of seats allotted to different Test Qualified candidates separately [AIEEEE/CET(State conducted test / University tests) / Association conducted test]**

*As per G.O.Ms.No.53, Higher Education,dated 10.05.2006, the following are the details of seats allotted to Convenor quota and Management quota.(Copy of the G.O.encoded)*

*No.of seats ear marked for Convenor quota equal to70% of total intake sanctioned in each programme.*

*No.of seats earmarked for Management quota equal to 30% of total intake sanctioned in each programme.*

❖ **Calendar for admission against management / vacant seats:**

•Last date for request for applications.	Last date for request for applications. Last date for submission of application. Dates for announcing final results. Release of admission list (main list and waiting list should be announced on the same day)
•Last date for submission of application.	
•Dates for announcing final results.	
Release of admission list (main list and waiting list should be announced on the same day)	

Date for acceptance by the candidate (time given should in no case be less than 15 days)	should be announced on the same day) Date for acceptance by the candidate (time given should in no case be less than 15 days)
•Last date for closing of admission.	
•Starting of the Academic session.	
•The waiting list should be activated only on the expiry of date of main list.	
•The policy of refund of the fee, in case of withdrawal, should be clearly notified.	

**XI. CRITERIA AND WEIGHTAGES FOR ADMISSION**

- ❖ Describe each criteria with its respective weightages i.e. Admission Test, marks in qualifying examination etc.
- ❖ Mention the minimum level of acceptance, if any.
- ❖ Mention the cut-off levels of percentage & percentile scores of the candidates in the admission test for the last three years.
- ❖ Display marks scored in Test etc. and in aggregate for all candidates who were admitted.

*Admissions are done strictly in accordance with the conditions laid down in the relevant Government Orders*

**Item No I-XI must be given in information brochure and must be hosted as fixed content in the website of the Institution.**

**The Website must be dynamically updated with regard to XII–XV.**

**XII. ApplicationForm :**

- ❖ Downloadable application form, with online submission possibilities.

**XIII. LIST OF APPLICANTS**

- ❖ List of candidates whose applications have been received along with percentile/percentage score for each of the qualifying examination in separate categories for open seats. List of candidates who have applied along with percentage and percentile score for Management quota seats.

*The admission process in respect of Management quota seats is carried out strictly in accordance With the conditions laid down in there Levant Government orders.*

*The list of the candidates along with percentage score will be placed on the web site shortly.*

**XIV. RESULTS OF ADMISSION UNDER MANAGEMENT SEATS / VACANT SEATS**

- ❖ Composition of selection team for admission under Management Quota with the brief profiles of members (This information be made available in the public domain after the admission process is over)
- ❖ Score of the individual candidates admitted arranged in order of merit.
- ❖ List of candidates who have been offered admission.
- ❖ Waiting list of the candidates in order of merit to be operative from the last date of joining of the first list candidates.

- ❖ List of the candidates who joined within the date, vacancy position in each category before operation of waiting list.

*Chairman- Secretary-Principal*

*the process is in progress. Details will be placed after the process over*

**XV. INFORMATION ON INFRASTRUCTURE AND OTHER RESOURCES AVAILABLE**

**LIBRARY:** ➤ Number of Library books / Titles / Journals available (All programme-wise):

➤I. Details of the library facilities:

S.N O.	Course Name	Number of Titles Available	Number of Volumes available	National Journals available	E-Journals available at actual
1	EEE	1175	5861	12	<b>J-GATE 7531</b>
2	MECH	1226	6877	12	
3	ECE	1525	7089	12	
4	CSE	1526	7011	12	
5	IT	1225	6247	7	
6	BME	550	2853	0	
7	CIVIL	583	4434	14	
9	Mining Engg	227	1603	5	
10	ECM	124	1092	4	
11	M.Tech (EPS)	475	1831	5	
12	M.Tech (CSE)	527	1805	5	
13	M.Tech (VLSI System Design)	425	1704	5	
14	M.Tech (SE)	376	1624	5	
15	M.Tech(CAD/CAM)	281	1461	5	
16	M.Tech(SA)	124	1092	5	
17	M.Tech(ES)	124	1092	5	
18	MBA (JNTU)	375	8102	10	
19	MCA	526	6374	10	
20	S&H	1156	9354	4	
	General Books	1236	2518	0	
	<b>Grand Total</b>	<b>13786</b>	<b>80024</b>	<b>137</b>	

➤List of online National / International Journals subscribed:  
*Online access to IEEE, IEEE Computer Society and ACM transactions.*

➤E-Library facilities: *Available*

**LABORATORY:**

For each Laboratory

➤List of Major Equipment/Facilities : *Copies enclosed*

➤List of Experimental Set up : *Copies enclosed*

**COMPUTING FACILITIES:**

➤Number and Configuration of Systems

*PIV- 1242 Nos.*

➤ Total number of systems connected to WAN

Tot Internet band width

*Leased line with a Band width of 24 Mbps*

- Major software packages available
- Special purpose facilities available

*Directline from SKYTELL provider.  
Listenclosed  
Project Development Center  
English Language Lab  
e-classroom with LCD Projector, T.V., Computers, etc.*

**WORKSHOP:** List of facilities available.

**Games and Sports Facilities**

**SPORTS FACILITIES AVAILABLE IN COLLEGE**

**CAMPUS: Indoor facilities**

---

1	Chess	
2	Caroms	
3	Table Tennis	for Men & Women

**II. Outdoor facilities**

---

1	Volley Ball	
2	Shuttle Badminton	
3	Bal Badminton	
4	Cricket	for Men
5	Foot Ball	
6	Tenikoit	
7	ThrowBall	
8	Kabadi	for Women

**III. Weight Tiring Hall**

---

1	Dumbles	
2	Bench press	
3	Incliner Benchpress	
4	Deliver benchpress	
5	Free plates & Rods	

**IV. Separate Sports room for Ladies**

---

1	Table Tennis	
2	Caroms	for Women
3	Chess	

**V. Track and field**

---

1	200m Track.	
2	Long Jump pit	
3	Javelin sector	
4	Shotput Sector	
5	Discuss throwing Sector	for Men & Women
6	Relayevents	

➤ **Extra Curriculum Activities**

*Preparation of CDs, Avishkar in house news letter, cultural activities etc.*

➤ **Soft Skill Development Facilities**

*Available*

➤ **Number of Classrooms and size of each**

*Rooms -80, Class Room size :for B.Tech classes 66 Sqm, for M.Tech classes 27.5 Sqm*

➤ **Number of Tutorial rooms and size of each**

*31 Nos.*

➤ **Number of laboratories and size of each**

*2Nos.- 209+207 sqm*

➤ **Number of drawing halls and size of each**

*1No. - 190sqm*

➤ **Number of Computer Centres with capacity of each**

*2 Rooms of each 162 sqm one room of 66 Sqm*

➤ **Central Examination Facility, Number of rooms and capacity of each.** *For academic section, different class rooms and Drawing hall conducting examinations available*

➤ **Teaching Learning process**

*Audio visual, E-class room, Internet lab, arranging Guest lectures.*

➤ **Curricula and syllabi for each of the programmes as approved by the University**

*- Copies enclosed*

➤ **Academic Calendar of the University**

*- Copies enclosed*

**Internal Continuous Evaluation System and place**

*For each Post Graduate programme give the following:*

1). Title of the program: **M.Tech** (ELECTRICAL POWER SYSTEMS.)

2) Curriculum and syllabus: *Copies of the syllabus is enclosed herewith*

3) Faculty Details:

EPS I Semester :

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1.	Dr.S.Vathsal	Professor & HOD	Advanced Control System
2.	T.S.Sastry	Assoc.Professor	Energy Audit Conservation Management
3.	G.Anand	Assoc.Professor	Micro Processors & Micro Controllers
4.	Dr.S.Shiva Prasad	Professor	Flexible AC Transmission Systems
5.	Dr.K.S.Gandhi	Professor	Reliability Engineering
6.	M.Vinod Kumar	Asst.Professor	Distribution Automation

EPS II Semester :

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1.	Dr.S.Vathsal	Professor & HOD	Power Quality
2.	T.S.Sastry	Assoc.Professor	Advanced Power System Protection
3.	Dr.S.Shiva Prasad	Professor	High Voltage DC Transmission
4.	R.Suresh Babu	Assoc.Professor	Power System Control & Stability
5.	Kiran Kumar Jain	Assoc.Professor	Extra High Voltage Transmission
6.	Dr.K.S.Gandhi	Professor	Power System Reliability

➤ **Laboratory facilities exclusive to the PG Programme.**

**Machines & Power Systems Lab (M.Tech/I-semester)/  
Electrical systems simulation lab (IIsem)**

➤ **Special Purpose:**

Software

S.No	Software	Design tools



1	MATLAB 6.5	Power System Toolbox Control system Toolbox Neural Network Toolbox
		Fuzzy logics Toolbox ...
2	GECE's Private LTD.	POWER GIS POWER NET POWER TRANS
3	MultiSIM2001	

- Academic Calendar and frame work: -Copies enclosed.
- Research focuses List of typical research projects:
  1. “Neural Network-Controlled Unified Power Quality Conditioners for system Harmonics Compensation”
  2. “Protection of Distribution transformers using SCADA”
  3. “Design of Drive system for Shunting Locomotive”
  4. “Reliability evolution of Transmission system”
  5. Reduction of Transients in High Voltage Induction motor.
  6. Reduction of Transients in Gas Insolated substation.
- Industry Linkage: -We have an III cell is established in the year 2006.
- Publications if any out of research in last three years out of master’s project: *Nil*
- Placement status : Since 1<sup>st</sup> outgoing batch is from the academic year 2014-15, efforts are being made to get the campus placements from this year.
- Admission Procedure: Eligibility is B.Tech/BE EEE Branch only. Preference is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech. Course is insisted upon.
- Fee structure: 18 seats of M.Tech. are distributed as, (a) general 13, to be filled by Convenor PG (Engg)-Admissions (b). 5 seats are to be filled by Management out of which 3 seats are earmarked for NRI candidates and 2 seats are allotted for sponsored candidates. Tuition Fee is charged @Rs.28500/- per semester in respect of all seats. A sum of Rs.10000/- is collected towards admission and miscellaneous fees, has onetime payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.
- Hostel facilities: Available - for both Men & Women.
- Contact address of Coordinator of the PG programme:

Name :Dr.J.Bhagwan Reddy, Professor EEE department  
 Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.  
 Telephone : 08413 – 235127, 235755  
 Email : [hodeee@jbiet.edu.in](mailto:hodeee@jbiet.edu.in)

- 1). Title of the program: **M.Tech (Energy System.)**
- 2) Curriculum and syllabus: *Copies of the JNTU syllabus is enclosed herewith*
- 3) Faculty Details :

ES I Semester :

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1.	P.S.Kumar	Assoc.Professor	Applied Thermo Dynamics
2.	Kiran Kumar Jain	Assoc.Professor	Nuclear Energy
3.	K.Giri Babu	Asst.Professor	Industrial Instrumentation & Control Engg.
4.	Dr.Indreswaraiah	Professor	Advanced Heat & Mass Transfer
5.	G.Rajashekar	Asst.Professor	Renewable Energy Technology
6.	Dr.S.Shiva Prasad	Professor	Thermal Power Plant

➤ **Laboratory facilities exclusive to the PG Programme.**

**Machines & Power Systems Lab (M.Tech/I-semester)/  
Electrical systems simulation lab (II sem)**

➤ **Special Purpose:**  
Software

S.No	Software	Design tools
1	MATLAB 6.5	Power System Toolbox Control system Toolbox Neural Network Toolbox
		Fuzzy logics Toolbox ...
2	GECE's Private LTD.	POWER GIS POWER NET POWER TRANS
3	MultiSIM2001	

- Academic Calendar and frame work: -Copies enclosed.
- Research focuses List of typical research projects:
  1. "Neural Network-Controlled Unified Power Quality Conditioners for system Harmonics Compensation"
  2. "Protection of Distribution transformers using SCADA"
  3. "Design of Drive system for Shunting Locomotive"
  4. "Reliability evolution of Transmission system"
  5. Reduction of Transients in High Voltage Induction motor.
  6. Reduction of Transients in Gas Insolated substation.
- Industry Linkage: -We have an III cell is established in the year 2006.
- Publications if any out of research in last three years out of master's project: *Nil*
- Placement status: Since 1<sup>st</sup> outgoing batch is from the academic year 2014-15; efforts are being made to get the campus placements from this year.

- Admission Procedure: Eligibility is B.Tech/BE EEE Branch only. Preference is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech Course is insisted upon.
- Fee structure: 18 seats of M.Tech. are distributed as, (a) general 13, to be filled by Convenor PG (Engg)-Admissions (b). 5 seats are to be filled by Management out of which 3 seats are earmarked for NRI candidates and 2 seats are allotted for sponsored candidates. Tuition Fee is charged @ Rs.28500/- per semester in respect of all seats. A sum of Rs.10000/- is collected towards admission and miscellaneous fees, has one-time payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.
- Hostel facilities: Available - for both Men & Women.
- Contact address of Coordinator of the PG programme:

Name Dr.J.Bhawan Reddy, Professor, EEE Department.  
 Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.  
 Telephone: 08413 – 235127, 235755  
 Email : [hodeee@jbiet.edu.in](mailto:hodeee@jbiet.edu.in)

1). Title of the program : **M.Tech (Computer Science & Engineering)**

2) Curriculum and syllabus: *Copies of the JNTU syllabus is enclosed herewith*

3) Faculty Details :

CSE I Sem:

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.Niraj Upadhyaya	Professor	Embedded Systems
2	Dr.Appa Rao Naidu	Professor	Object oriented modeling
3	G.Sreenivasulu	Assoc.Professor	Java Web Technologies
4	A.Ramesh Babu	Assoc.Professor	Advanced compiler Design
5	B.Nageshwar Rao	Assoc.Professor	Computer System Design
6	G.Sreenivasulu B.Ravindra Kumar	Assoc.Professor Asst.Professor	Java Web Technologies lab

CSE II Sem:

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.Niraj Upadhyaya	Professor	Advanced Computer Architecture
2	G.Sreenivasulu	Assoc.Professor	Semantic Web & Social Networks
3	A.Ramesh Babu	Assoc.Professor	Advanced Computer Networks
4	B.Nageshwar Rao	Assoc.Professor	Information Security
5	B.Ravindra Kumar	Asst.Professor	Distributed Data Base
6	B.Madhavi Devi	Asst.Professor	Distributed Computing
7	B.Ravindra Kumar / A.Ramesh Babu	Asst.Professor Asst.Professor	Data Base & Compiler Design Lab.

**M.Tech Lab  
Facilities :  
Computer Lab:**

S.No.	Software
1	My Sql
2	P-IV-1.96H330nos
3	128MBRAM
4	40 GB HOD
5	1BMRationalRose
6	JAVA 1.5
7	EclipseSDK

**Special  
Purpose:  
Software**

S.No.	Software
1	WirelessXP
2	Windows 2000 Professional
3	Linex SUSE
4	Msoffice / Turbo C ++
5	Netbeans

- Academic Calendar and frame work: -Copies enclosed
- Research focuses List of typical research projects: ---
- Industry Linkage: - We have an III cell is established in the year 2006.
- Publications if any out of research in last three years out of master's project: *Nil*
- Placement status : Placement efforts are in place
- Admission Procedure: Eligibility is B.Tech/BE all branch. Preference admission is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech. course is insisted upon.
- Fee structure: 18 seats of M.Tech are distributed as,(a) general 13,tobe filled by Convenor PG(Engg)-Admissions (b). 5 seats are to be filled by Management out of which 3 seats are earmarked for NRI candidates and 2 seats are allotted for sponsored candidates. Tuition Fee is charged@Rs.28500/-per semester in respect of all seats. A sum ofRs.10000/-is collected towards admission and miscellaneous fees, has onetime payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.
- Hostel facilities: Available - for both Men & Women.
- Contact address of Coordinator of the PG programme:

Name : Mr.G.Srinivasulu, Professor, CSE Department.  
Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.  
Telephone : 08413 – 235127, 235755  
Email : [hodcse@jbiet.edu.in](mailto:hodcse@jbiet.edu.in)

- 1). Title of the program : **M.Tech (VLSI System Design)**  
**Curriculum of Syllabi** : *Copies of the JNTU syllabus is enclosed herewith*

VLSI I Semester:

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.D.N.Rao	Professor & HOD	VLSI Technology & Design
2	B.Shravan Kumar	Assoc.Professor	MESD
3	B.Kishore Kumar	Asst.Professor	Algorithm for VLSI Automation
4	Chenna Keshavulu	Assoc.Professor	DSD
5	R.Nageshwar Rao	Asst.Professor	CPLD & FPGA Architecture
6	Mohd Ali	Asst.Professor	Network Security & Cryptography
7	B.Kishore Kumar	Asst.Professor	Simulation Lab.

VLSI II Semester

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.D.N.Rao	Professor & HOD	DFTS
2	K.Snehalatha	Assoc.Professor	IM& VP
3	B.Kishore Kumar	Asst.Professor	LP – VLSI
4	B.Shravan Kumar	Assoc.Professor	SOC
5	G.Anand	Assoc.Professor	ERTS
6	P.Kranthi Kumar	Asst.Professor	Embedded System Lab.

**Lab Facilities:**

Xerox + Scanner + Printer	: 01
CPLD Trainer Kits	: 08
CPLD FPGA Training Kits	: 05
FPGA Trainer Kits	: 08
Xilinx 7.1 + Model sim6.0a	: 02
Shoe Racks	: 36
Computer Tables	: 18
Computer Chairs	: 35
UPS	: 02
Staff Chairs	: 04

- Academic Calendar and frame work : -Copies enclosed
- Research focuses List of typical research projects: Nil
- Industry Linkage: - We have an IICell is established in the year 2006.
- Publications if any out of research in last three years out of master's project:
- Admission Procedure: Eligibility is B.Tech/BE all branch. Preference admission is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech. course is insisted upon.
- Fee structure: 24 seats of M.Tech. are distributed as, (a) general 13, to be filled by Convenor PG(Engg)-Admissions (b). 5 seats are to be filled by Management out of

which 3 seats are earmarked for NR candidates and 2 seats are allotted for sponsored candidates. Tuition Fees charged @ Rs.28500/- per semester in respect of all seats. A sum of Rs.10000/- is collected towards admission and miscellaneous fees, has one time payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.

• Hostel facilities: Available - for both Men & Women.

• Contact address of Coordinator of the PG programme:

Name : **Dr.D.N.Rao**, Assoc. Professor, ECE Department.

Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.

Telephone : 08413 – 235127,235755

Email : [hodece@jbiet.edu.in](mailto:hodece@jbiet.edu.in)

1). Title of the program : **M.Tech (Software Engineering)**

2) Curriculum and syllabus: *Copies of the JNTU syllabus is enclosed herewith*

Soft ware Engg. I Semester:

S.No.	NAME	DESIGNATION	SUBJECTTAUGHT
1	M.A.Muneer	Asst.Professor	ACN
2	V.Krishna Reddy	Assoc.Professor	DD
3	B.Madhavi Devi	Assoc.Professor	JWT
4	K.Roshan	Assoc.Professor	S & PM
5	P.Prakash	Asst.Professor	OOM
6	Ch.Srinivasulu	Assoc.Professor	S & E
7	Ch.Srinivasulu B.Madhavi Devi	Assoc.Professor Asst.Professor	JWT Lab.

Soft ware Engg. II Semester:

S.No.	NAME	DESIGNATION	SUBJECTTAUGHT
1	M.A.Muneer	Asst.Professor	SQAT
2	V.Krishna Reddy	Assoc.Professor	SADP
3	B.Madhavi Devi	Assoc.Professor	DC
4	K.Roshan	Assoc.Professor	INN
5	Ch.Srinivasulu	Assoc.Professor	SOA
6	Ch.Srinivasulu M.A.Muneer	Assoc.Professor Asst.Professor	UML & ST Lab

### Laboratory facilities exclusive to the PG

#### Programme. Computer Lab:

S.No.	Software
1	Oracle9Enterprise
2	
3	PIV-1.9 GHZ 30 nos. 128 MB RAM
4	40GBHarddiskDrive
5	IBM Rational Rose
6	JAVA 1.5
7	Eclipse SDK.

## Special Purpose: Software

S.No.	Software
1	Window XP
2	Windows 2000 Professional
3	Mandriva/Redhat LINUX /Sun Solaris10.0
4	MS office /CNet beans
5	TurboC++

- Academic Calendar and framework : -Copies enclosed
- Research focuses List of typical research projects: ---
- Industry Linkage: - We have an III cell is established in the year 2006.
- Publications if any out of research in last three years out of master's project: *Nil*
- Placement status: Sincethis1<sup>st</sup> Batch, placement efforts will be made during next academic year i.e. during 2009-10.
- Admission Procedure: Eligibility is B.Tech/BE all branch. Preference admission is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech. course is insisted upon.
- Fee structure: 18 seats of M.Tech are distributed as,(a)general13, to be filled by Convenor PG(Engg)-Admissions (b). 5 seats are to be filled by Management out of which 3 seats are earmarked for NRI candidates and 2 seats are allotted for sponsored candidates.TuitionFeeischarged@Rs.28500/-per semester in respect of all seats. A sum of Rs.10000/-is collected towards admission and miscellaneous fees, has onetime payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.
- Hostel facilities: Available - for both Men & Women.
- Contact address of Coordinator of the PG programme:  
  
Name : Mr.Ch.Srinivasulu, Assoc.Professor, CSE Department.  
Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.  
Telephone : 08413 – 235127, 235755  
Email : [hodit@jbiet.edu.in](mailto:hodit@jbiet.edu.in)



1). Title of the program : **M.Tech (CAD /CAM)**

2) Curriculum and syllabus: *Copies of the JNTU syllabus is enclosed herewith*

**M.Tech (CAD/CAM) – I Semester:**

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.C.Udaya Kiran	Professor	Manufacturing methods and <del>Mechanics of composites</del>
2	G.Gopala Krishna	Assoc.Professor	Advanced CAD
3	P.Divakara Rao	Assoc.Professor	Finite Element Analysis
4	B.V.K.Murthy	Asst.Professor	Precision Engineering
5	Y.Vijaya Kumar	Assoc.Professor	Advanced Mechanics of Solids

**M.Tech (CAD/CAM) – II Semester:**

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.C.Udaya Kiran	Professor	Flexible Manufacturing System
2	G.Gopala Krishna	Assoc.Professor	Computer Aided Manufacturing
3	P.Divakara Rao	Assoc.Professor	Computer Aided Process Planning
4	B.V.K.Murthy	Asst.Professor	Industrial Robotics
5	Y.Vijaya Kumar	Assoc.Professor	Production and operating management

**Laboratory facilities exclusive to the PG Programme.**

**Computer Lab:**

S.No.	Software
1	Solidworks(2009-10) → I Semester
2	CAM & Robotic Simulation S/W → IISemester

- Academic Calendar and frame work : -Copies enclosed
- Research focuses List of typical research projects: ---
- Industry Linkage: - We have an III cell is established in the year 2006.
- Publications if any out of research in last three years out of master's project: *Nil*
- Placement status: Since this is 1<sup>st</sup> Batch, placement efforts will be made during next academic year i.e. during 2008-09.
- Admission Procedure: Eligibility is B.Tech /BE all branch. Preference admission is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech. course is insisted upon.
- Fee structure: 18 seats of M.Tech. are distributed as, (a) general 13, to be filled by Convenor PG(Engg)-Admissions (b). 5 seats are to be filled by Management out of which 3 seats are earmarked for NRI candidates and 2 seats are allotted for sponsored candidates. Tuition Fee is charged @Rs.28500/- per semester in respect of all seats. A sum of Rs.10000/- is collected towards admission and miscellaneous fees, has onetime payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.
- Hostel facilities: Available - for both Men & Women.

- Contact address of Coordinator of the PG programme:

Name : **Dr.C.Udaya Kiran**, Professor, Mech Department.  
 Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.  
 Telephone : 08413 – 235127, 235755  
 Email : [hodmech@jbiet.edu.in](mailto:hodmech@jbiet.edu.in)

Title of the program: **M.Tech(Bio-Medical Signal Processing & Instrumentation)**

2) Curriculum and syllabus: *Copies of the JNTU syllabus is enclosed herewith*

3) Faculty Details: BMSP&II Semester:

S.No.	NAME	DESIGNATION	SUBJECTTAUGHT
1.	G.Keerthi Kalyani	Asst.Professor	Clinical Instrumentation
2.	M.Deepthi	Asst.Professor	Virtual Instrumentation
3.	M.Deepthi	Asst.Professor	Bio-sensors
4.	J.Manikyam	Asst.Professor	Medical Image Processing
5.	D.Priyanka	Asst.Professor	Physiology for Engineers
6.	Kranthi Kumar	Asst.Professor	Real Time signal processing

BMSP&I - II Semester :

S.No.	NAME	DESIGNATION	SUBJECTTAUGHT
1.	G.Keerthi Kalyani	Asst.Professor	Advance Medical Imaging
2.	J.Manikyam	Asst.Professor	Advance biomedical signal processing
3.	M.Deepthi	Asst.Professor	Fiber optics and laser instrumentation
4.	M.Deepthi	Asst.Professor	Rehabilitation Engineering
5.	J.Manikyam		Digital signal processors and architectures
6.	G.Keerthi Kalyani		Design and Analysis of signal conditions ckts.

➤**Laboratory facilities exclusive to the PG Programme.**

**Machines & Power Systems Lab (M.Tech/I-semester)/  
 Electrical system simulation lab (IIsem)**

➤Special Purpose: Software

Laboratory facilities exclusive to the PG Programme:

- 1) Bio medical and virtual instrumentation lab (I Sem) I/C: M.Deepthi
- 2) Advance medical signal and image processing lab (II Sem) I/C: G.Keerthi Kalyani, Manikyam

- Academic Calendar and frame work: -Copies enclosed.

- Research focuses List of typical research projects:

- Industry Linkage: -We have an III cell is established in the year 2006.

- Publications if any out of research in last three years out of master's project: *Nil*

- Placement status : Since 1<sup>st</sup> outgoing batch is from the academic year 2005-06, efforts are being made to get the campus placements from this year.

- Admission Procedure: Eligibility is B.Tech/BE EEE Branch only. Preference is given to the GATE qualified students and on merit basis of GATE score. If GATE qualified candidates are not available, then Non-GATE students with a minimum of 50% in the aggregate of B.Tech. Course is insisted upon.
- Fee structure: 18 seats of M.Tech. are distributed as, (a) general 13, to be filled by Convenor PG(Engg)-Admissions (b). 5 seats are to be filled by Management out of which 3 seats are earmarked for NRI candidates and 2 seats are allotted for sponsored candidates. Tuition Fee is charged @ Rs.28500/- per semester in respect of all seats. A sum of Rs.10000/- is collected towards admission and miscellaneous fees, has onetime payment at the time of admission. GATE qualified students are eligible for AICTE GATE scholarships.
- Hostel facilities: Available - for both Men & Women.

1). Title of the program: **MASTER OF COMPUTER APPLICATIONS**

2) Curriculum and syllabus: *Copies of the JNTU syllabus is enclosed herewith*

MCA – I Semester:

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.Sumagna Patnaik	Professor	Mathematical foundation
2	M.Chandrashekar	Asst.Professor	OOPS through C++
3	Chinmayee Pramanik	Asst.Professor	Computer organization
4	P.Subramanyam	Asst.Professor	AFM
5	T.Bhuvaneswari	Asst.Professor	P & S

MCA – II Semester

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Ch.Radha Krishna	Assoc.Professor	DBMS
2	M.Ravi	Assoc.Professor	JAVA
3	M.Ravi	Assoc.Professor	CN
4	Chinmayee Pramanik	Asst.Professor	LINUX
5	M.Chandrashekar	Asst.Professor	SE

➤ **Laboratory facilities exclusive to the PG Programme.**

PIV-1.9GHz                      **30Nos**  
 128MBRAM  
 40GB Harddisk Drive  
 MBFDD  
 Keyboard & Mouse  
 15" Color Monitor

Printer- 01No  
 Switches-02Nos

**Special Purpose: Software**

S.No	Software
1	Windows 98
2	Windows2000 Professional
3	Mandrake/ Red hat LINUX / Sun Solaris 10.0
4	MS Office XP
5	VB Studios / c# studio
6	Turbo C++
7	Oracle 9 Enterprise
8	Norton Antivirus
9	UML Rational Rose
10	JAVA 1.5
11	Eclipse SDK

•Academic Calendar and framework : -Copies enclosed

•Research focuses List of typical research projects: ---

.Industry Linkage: - We have an III cell is established in the year 2006.

•Publications if any out of research in last three years out of master's project: *Nil*

• Placement status: Since this is 1<sup>st</sup> Batch, placement efforts will be made during next academic year i.e. during 2008-09.

•Admission Procedure &Fees Structure:

TUITION FEE	
University / Govt.	Self Finance
Rs.5000/- + 1500	Rs.34300 /- per year

OTHER FEE		
Particular	SC / ST	Others
Registration fee	Rs.100/-	Rs.200/-
Counselingfee	Rs.300/-	Rs.500/-
Cancellation fee	Rs.1000/-	Rs.2000/-

•Hostel facilities: Available - for both Men & Women.

•Contact address of Coordinator of the PG programme:

Name : Dr.Sumagna Patnaik, Professor, MCA Department.

Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.

Telephone : 08413 – 235127, 235755

Email : [hodmca@jbiet.edu.in](mailto:hodmca@jbiet.edu.in)

1). Title of the program: **MASTER OF BUSINESS ADMINISTRATION**

2) Curriculum and syllabus:

ENCLOSED MBA

Department I Semester :

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.Durga Prasad	Professor & HOD	Business Law &
2	Mr.Rajesh Pershad	Assoc.Professor	MOB
3	Mr.V.V.Subramanyam	Assoc.Professor	ME
4	Mr.Raghuram Reddy	Asst.Professor	FAA
5	Ms.T.Bhuvanewari	Asst.Professor	RMSA

**MBA Department III Semester :**

S.No.	NAME	DESIGNATION	SUBJECT TAUGHT
1	Dr.Durga Prasad	Professor & HOD	SAPM
2	Mr.Rajesh Pershad	Assoc.Professor	RM & SM
3	Mr.V.V.Subramanyam	Assoc.Professor	Deraviitatives, CMA.
4	Mr.P.Subramanyam	Assoc.Professor	EGG, MT
5	Mr.Raghuram Reddy	Asst.Professor	FIMS
6	Ms.T.Bhuvanewari	Asst.Professor	R & S

➤ Academic Calendar and frame work: -Copies enclosed

- Research focuses List of typical research projects: ---
- Industry Linkage:- We have an III cell is established in the year 2006.
- Publications if any out of research in last three years out of master's project: *Nil*
- Placement status: Since this is 1<sup>st</sup> Batch, placement efforts will be made during next academic year i.e. during 2013-14.

• Admission Procedure & Fees Structure:

TUITION FEE	
University / Govt.	Self Finance
Rs.5000/- + 1500 (Infrastructure fee)	Rs.45000 /- per year

- Hostel facilities: Available - for both Men & Women.
- Contact address of Coordinator of the PG programme:  
Name : Dr.Durga Prasad in MBA Department  
Address : JBIET, Yenkapally, Moinabad Mandal, R.R. Dist., Hyd-75.  
Telephone : 08413 – 235127, 235755  
Email : [hodmba@jbiet.edu.in](mailto:hodmba@jbiet.edu.in)