



J.B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous

(Accredited by NBA & NAAC, Approved by AICTE & Permanently Affiliated to JNTUH)

Bhaskar Nagar, Yenkapally (V), Moinabad (M), P.O. Himayath nagar, R.R. District, Hyderabad-5000075.

Department of Electrical and Electronics Engineering

Webinar Report on

JOB OPPORTUNITIES IN VLSI / SEMICONDUCTOR INDUSTRY

Wednesday 05th FEBRUARY, 2025 @ 02:00 PM

Venue: Placement Seminar Hall.

Resource Person

Mr. SATISH KODETI

**Physical Design Engineering,
Ambit Semiconductors Pvt. Ltd,
Bangalore.**

Co-ordinator

**K. Laxmi Prasanna,
Assistant Professor.**

Convener

**Dr. P. Duraipandy,
Associate Professor & Head of the Department.**

PREAMBLE:

The Department of Electrical and Electronics Engineering was conducted the Webinar on ***“JOB OPPORTUNITIES IN VLSI / SEMICONDUCTOR INDUSTRY”*** for, Students of UG and PG courses.

J.B. Institute of Engineering and Technology was established in 1998, popularly known as JBIET, is a UGC approved Autonomous Institution. JBIET is an NBA and NAAC accredited Institution. The Institute has various laboratories, workshops and other support systems to provide a hands-on experience. It can boast of its well qualified and expert faculty who inculcate the best values and principles. The Institution aims at the holistic development of the students and has a notable Placement track record. The institute also nurtures and promotes Entrepreneurship, provides end to end support in the form of funding for start-ups. JBIET has been ranked consistently among top Institutions in Telangana and India.

Department of Electrical and Electronics Engineering is one of the oldest departments of JBIET, started in 1998 with a team of highly experienced faculty members. Department offers one UG program (B. Tech – EEE) and one PG program (M. Tech – EPS), with a present intake of 120 UG and 18 PG students. It is accredited by NBA under Tier – I. The department organized various programs such as Faculty development programs, hands-on sessions and technical workshops to strengthen the overall development of the Students and Faculty.

Mr. SATISH KODETI, Physical Design Engineering, Ambit Semiconductors Pvt. Ltd, Bangalore delivered the webinar.

➤ **The course content delivered in a webinar is as follows:**

The importance of Job Opportunities in VLSI / Semiconductor Industry is discussed in this session.

The Speaker of the session **Mr. SATISH KODETI** presented the keynote address on Job Opportunities in VLSI / Semiconductor Industry. He discussed the major elements of chip design methods and production strategies in chip design. What is VLSI? How Semiconductor devices are manufactured using VLSI. Job opportunities in chip design and VLSI.

He inspired the students by lively interacting on topics such as:

- **High demand:** Due to the growing need for advanced electronic devices, VLSI engineers are in high demand across industries, leading to good job prospects.

- **Competitive salary:** VLSI engineers often receive competitive salaries, especially with experience and expertise in specialized areas.
- **Intellectual challenge:** VLSI design involves complex problem-solving and requires a deep understanding of electrical engineering principles, making it intellectually stimulating.
- **Continuous learning:** The field of VLSI is constantly evolving with new technologies and methodologies, necessitating a commitment to ongoing learning.
- **Impactful work:** VLSI engineers directly contribute to the design of chips that power various electronic devices, impacting the technology we use daily.




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 HOD - EEE
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 Bhaskar Nagar, Moinebad Mandal
 Hyderabad-500 075.

EVENT POSTER

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS
ENGINEERING**

**WEBINAR
On
JOB OPPORTUNITIES IN
VLSI/SEMICONDUCTOR INDUSTRY**

Resource Person

Mr. Satish Kondeti

Physical Design Engineer,
Ambit Semiconductors Pvt. Ltd.
Bangalore



5 February 2025



2 PM to 4 PM



**Placement Seminar
Hall**



<https://forms.gle/9ZXvBYDUxXAzCiGN8>

CIRCULAR



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CIRCULAR

Date: 04-02-2025

The students of II & III Year B. Tech EEE are hereby informed that there will be a Webinar on “***JOB OPPORTUNITIES IN VLSI/SEMICONDUCTOR INDUSTRY***” on 05th February 2025 at 02:00 PM in Placement Seminar Hall. So, all students should attend and make it successful.


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Hyderabad-500 075.

PHOTOS OF SESSION





 **GPS Map Camera**

Ranga Reddy, Telangana, India
JBIET, Amdapur X Road , Yenkapally, Rangareddy , Telangana
500075, India., Ranga Reddy, Telangana , India
Lat 17.330131° Long 78.298728°
05/02/2025 02:51 PM GMT +05:30

