



J.B. INSTITUTE OF ENGINEERING & TECHNOLOGY
(UGC Autonomous)

DEPT. OF ELECTRONICS & COMMUNICATION ENGINEERING

CIRCULAR

Date: 30-10-2019

All B. Tech students are invited / informed that a workshop on "Industrial Application of VLSI" will be conducted on 2-11-2019 at 10.00AM

Venue: MNR AUDITORIUM.

Note: Circulate to faculties and students.

HOD ECE

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JB Institute of Engineering & Technology
(UGC Autonomous)
(Accredited by NAAC, Approved by AICTE,
Permanently Affiliated JNTUH)
Bhaskar Nagar, Moinsabad Mandal.
Hyderabad-500 075.



J.B.Institute of Engineering and Technology
(UGC AUTONOMOUS)

Department of Electronics & Communication Engineering

**Report
On
One day workshop on Industrial Applications of VLSI Design**

One day workshop on Industrial Applications of VLSI Design was conducted by Department of ECE on 2.11.19.

Dr. P.V Srinivasa, Principal of the college, during the inaugural address, ascertained that need of training. Dr. Towheed Sultana, Head of Electronics and Communication Engineering Department addressed about few Industrial Applications of VLSI Design.

The resource person was M Murali from Vedic school of VLSI Design. The objective this workshop is to provide an exposure to the Circuit Design & Simulation, Layout, Physical Verification (DRC, LVS), Extraction and hands-on experience on the state-of-the-art Cadence EDA tools for applications of VLSI Design over various industries. The topics covered are:

- Introduction to Cadence and its various tools.
- Basics of mixed signal design using Cadence Virtuoso.
- Introduction to Physical Verification, DRC/LVS by Assura.
- Implementation of NC Launch and RTL Compiler for Digital Circuit using Verilog.
- Extraction of RC Components and Generation of GDSII file.

Coordinators **B.Kishore**, Assistant Professor and **Mr. K.Rajender**, Assistant Professor coordinated the program with their concerted efforts and made the program successful.

Towheed S

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CO-ORDINATORS

1. **MR. B.KISHORE**
Assoc. Professor, ECE
2. **MR. K.RAJENDER**
Asst. Professor, ECE

ORGANIZING COMMITTEE

1. **Mr. G. ANAND**
Assoc. Professor, ECE
2. **Mr. RAVI KIRAN**
Asst. Professor, ECE
3. **Mr. VVVS. PRASAD**
Assoc. Professor, ECE
4. **Mr. RAJENDER R**
Asst. Professor, ECE
5. **Mr. A. SOMASHEKHAR**
Asst. Professor, ECE
6. **Mrs. B. SOWMYA**
Asst. Professor, ECE
7. **Mrs. G. SAMATHA**
Asst. Professor, ECE

CHIEF PATRONS

1. **Shri. J. V. Krishna Rao,**
Honourable Secretary,
J. B. Educational Society.

PATRONS

1. **Dr. V. V. RAO, CEO, JBGEI.**
2. **Dr. P. V. SRINIVASA,**
Principal, JBIET.

ORGANIZER

DR. TOWHEED SULTANA, HOD – ECE

Organized by

**Department of Electronics and
Communication Engineering in
Association with Vedic School of VLSI
Design**



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BHASKAR NAGAR, YENKAPALLY (V), MOINABAD (M),
R. R. DIST., HYDERABAD.

Department of Electronics and Communication
Engineering



**One day workshop on
Industrial Applications of VLSI
Design**

DATE: 2-11-2019

DEPARTMENT OF ECE, JBIET

Keynote Speaker

DR. P. RADHA KRISHNA

About JBIET

Spread across huge green and clean cinematic campus, The J.B. Institute of Engineering and Technology is a UGC Autonomous Institute accredited by NAAC, approved by AICTE & permanently Affiliated by JNTU, Hyderabad. JBIET is one among the best 10 Engineering Colleges of the Telangana State, India, continues to strive to impart technical and professional education of very high standards. The institute is also the most preferred institution for aspiring students and their parents and has earned itself the reputation of being a centre of academic excellence in many ways over the years.

The Placement record of JBIET over the years is a proof of our right efforts in enabling the best in the class engineering, technical and professional education to aspirants.

ABOUT THE DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

The Department of ECE is best known for its talented and dedicated professionals renowned for their excellence in various specializations in the field of Electronics & Communication Engineering. The Department is provided with state of the art technological tools incorporated in all the existing laboratories. For the last ten years, the students of ECE, who walked out of

the portals of the institute successfully, holding their degrees, were immediately inducted into the MNCs of high reputation in India & abroad. The intake of B.Tech Program is 120 and of M.Tech program in VLSI System Design is 24. Department of ECE is having professional societies like IEEE

About workshop:

The Advancement of science & technology is like a boon to the world, as human beings come to know a lot about the world they are living in including the activities they indulge into. Furthermore, the development of technology along with the advancement in Science helps to bring in a revolution in various fields such as medicine, agriculture, education, information and technology, and many more

Objective:

The aim of this workshop is to provide hands-on experience on the state-of-the-art Cadence EDA tools for applications of VLSI Design over various industries.

The participants will have an exposure to the Circuit Design & Simulation, Layout, Physical Verification (DRC, LVS), and Extraction.

The workshop includes practice sessions on the Cadence design and simulation tools

(Encounter, RTL Compiler, Virtuoso, Spectre, Assura and Incisive).

PROGRAMME DETAILS

Date	Sessi on Time	Details of Session
02.11.2019	9:30 to 10:30	Inauguration and dignitaries speech
02.11.2019	10:30 to 12:30	Introduction to VLSI design and Applications of VLSI through back end
02.11.2019	1.30 to 03.30	Hands on session by using cadence
02.11.2019	3.30 to 04.00	Valedictory and Feedback

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Bhaskar Nagar, Yenkepally Village, Moinabad Mandal, R.R.District, Hyderabad-500075

Department of Electronics and Communication Engineering

Industrial Applications of VLSI Design

2-11-2019

List of Participants

Sl. No	Name of the Participant	Roll number	Attendance
1	MOHD ABDULKAREEM	15671A0474	P
2	CHANDRASHEKAR . U	15671A0490	P
3	KAUSHIK PRABHU BAILUR	15671A0496	A
4	CHILAKAMARI GNANESHWAR	16675A0403	P
5	HARINIREDDY	15671A04A2	P
6	K.PURUHITHIKA	15671A0454	P
7	N ANJALI	15671A0404	P
8	ORUGANTI VYASA DATTA	15671A0407	A
9	MD ZAKIR HUSSAIN	15671A0413	A
10	RISHABH TIWARI	15671A0434	A
11	R.SANTOSH PREETHAM	15671A0441	A
12	MOTHE SRIKANTH	16675A0415	P
13	KUTURU SAI POOJA	15671A0438	P
14	MANPREET KOUR	15671A0459	P
15	SOMA PRIYANKA	15671A0408	P
16	GAJJA MAHESH KUMAR GOUD	16671A0470	P
17	GUDURU RAHUL RAJ	16671A0471	P
18	KANDHIMALLA JESMITHA REDDY	16671A0472	A
19	KUNA LAVANYA	15671A0453	A
20	RAGULA SAI TEJA	15671A0457	P
21	BOMMAGANI SATHISH	15671A0466	P
22	MALKAGIRI VENNELA	16671A0479	P
23	MYLAVARAPU SATYA SAI MOUNICA	16671A0480	P
24	NARIGENNAGARI SAMINA	16671A0481	P
25	BODDUPALLY AKHIL KUMAR	15671A0468	P
26	GAJULA ANJALI	15671A0472	P
27	BANNARAVURI HARIVARDHAN	15671A0476	P
28	PATHLAVATH SUNITHA	15671A0487	A
29	M.MAHESH	15671A0492	A
30	A AKHILA REDDY	16671A0401	P
31	A KEERTHANA	16671A0402	P
32	KONDERU RAJ KUMAR	16671A0404	P

33	P SANTOSH CHAITANYA	16671A0483	P
34	PIPPARI MANISHA	16671A0484	P
35	RAIHALAYA NAVEEN	16671A0485	P

Towheeds

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