

JB INSTITUTE OF ENGINEERING AND TECHNOLOGY R18
UGC AUTONOMOUS, EXAMINATION BRANCH

TIME-TABLE OF I M.Tech. I-Sem Supplementary Examinations –R18-2019, 2018 BATCH – OCT/NOV.2021


Time: 10:00 AM to 01.00 PM

Branch	EEE	ME	ECE	CSE
Course Date/Day	ELECTRICAL POWER SYSTEMS	CAD/CAM	VLSISD	COMPUTER SCIENCE & ENGINEERING
22-10-2021 FRIDAY	ADVANCED POWER SYSTEM ANALYSIS	ADVANCED CAD	DIGITAL SYSTEM DESIGN USING HDL	ADVANCED DATA STRUCTURES
25-10-2021 MONDAY	FLEXIBLE AC TRANSMISSION SYSTEM	ADVANCED COMPUTER AIDED MANUFACTURING	DIGITAL CMOS INTEGRATED CIRCUIT DESIGN	CRYPTOGRAPHY AND NETWORK SECURITY
27-10-2021 WEDNESDAY	ENERGY AUDITING CONSERVATION AND MANAGEMENT	RAPID PROTOTYPING TECHNOLOGIES/ MECHANICAL BEHAVIOUR MATERIALS	DIGITAL SYSTEM DESIGN	MACHINE LEARNING
29-10-2021 FRIDAY	DISTRIBUTION AUTOMATION	AUTOMATION IN MANUFACTURING	VLSI TECHNOLOGY AND DESIGN	DISTRIBUTED SYSTEMS
01-11-2021 MONDAY	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR
03-11-2021 WEDNESDAY	DISASTER MANAGEMENT	DISASTER MANAGEMENT	PEDAGOGY STUDIES/ ENGLISH FOR RESEARCH PAPER WRITING	PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS

NOTE : 1) ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

Dt:- 09-10-2021


CONTROLLER OF EXAMINATIONS 9/10/21


CHIEF CONTROLLER OF EXAMINATIONS
PRINCIPAL
J.B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
Bhaskar Nagar, Yenkapally (V),
Moinabad (M), R.R. Dist.-500 075.

JB INSTITUTE OF ENGINEERING AND TECHNOLOGY
UGC AUTONOMOUS YENKAPALLY, MOINABAD – 500 075, HYDERABAD EXAMINATION BRANCH
TIME-TABLE OF I M.Tech. II-Semester SUPPLEMENTARY Examinations –R18-2019, 2018 BATCHES – OCT/NOV. 2021

Time: 10:00 AM to 01:00 PM

Branch	EEE	MECHANICAL	ECE	CSE
Course Date/Day	ELECTRICAL POWER SYSTEMS	CAD/CAM	VLSISD	COMPUTER SCIENCE & ENGINEERING
21-10-2021 THURSDAY	Advanced Power System Protection	Manufacturing Systems, Simulation modeling & Analysis	Analog CMOS Design	Advanced Algorithms
23-10-2021 SATURDAY	Power System Operation and Deregulation	Precision Engineering	ASIC Design	Network Programming
26-10-2021 TUESDAY	Value Education	Personality Development through Life Enlightenment Skills.	Value Education	Professional Ethics
Program Elective-III				
28-10-2021 THURSDAY	1. High Voltage Engineering 2. Power Quality 3.HVDC Transmission	1. Intelligent Manufacturing Systems 2. Special Manufacturing Process 3.Optimization Techniques & Applications	1LOE Power VLSI Design 2. Design for Testability 3.System on Chip Architecture	1.Digital Forensics 2. Data Analytics 3. Parallel Computing
Program Elective-IV				
30-10-2021 SATURDAY	1. Voltage Stability 2. AI Techniques in Electrical Engineering 3.Reactive Power Compensation and Management	1.Advanced Mechatronics 2. Design and Manufacturing of MEMS and Micro Systems 3.Design for Manufacturing & Assembly	1Hard ware Software Co-Design 2.Optimization Techniques in VLSI Design 3.Image and Video Processing	1.Human Computer Interaction 2. Computer Vision 3. Distributed Databases

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Dt: 09-10-2021


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Moinabad (M), F.R Dist.-500 075.



**J.B. INSTITUTE OF ENGINEERING & TECHNOLOGY
UGC AUTONOMOUS**

EXAM BRANCH – CIRCULAR

TIME-TABLE

**II M.TECH – I Semester (R18) 2019, 2018 Batches -
SUPPLEMENTARY Examinations, OCT. 2021**

Time: 02.00 PM to 05.00 PM

Date, Day & Branch	CAD/CAM	EPS	VLSISD	CSE
22-10-2021 FRIDAY	PROGRAM ELECTIVE-V Advanced Industrial Robotics	PROGRAM ELECTIVE-V Modern Control Theory/ Reliability Engineering	PROGRAM ELECTIVE-V CAD FOR VLSI/ Advanced Computer Architecture	PROGRAM ELECTIVE-V Blockchain Technology
25-10-2021 MONDAY	OPEN ELECTIVE Industrial Safety	OPEN ELECTIVE Renewable Energy Systems	OPEN ELECTIVE AD-HOC WIRELESS NETWORKS/ Digital Signal Processors and Architectures	OPEN ELECTIVE Python Programming

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the Undersigned.


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Dt: 09.10.2021


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**J.B. INSTITUTE OF
ENGINEERING & TECHNOLOGY**

Bhaskar Nagar, Yenkapally (V),

Hyderabad (M), R.R.D. Dist. 500075

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