

**JB INSTITUTE OF ENGINEERING AND TECHNOLOGY**  
**UGC AUTONOMOUS YENKAPALLY, MOINABAD – 500 075, HYDERABAD EXAMINATION BRANCH**  
**TIME-TABLE OF I M.Tech. I-Sem. Supply Examinations –R15-2017, 2016, 2015 BATCH – OCT 2020**

Time: 10:30 AM to 12.30 PM

Branch	EEE	MECHANICAL	ECE	CSE	IT
Course Date/Day	ELECTRICAL POWER SYSTEMS	CAD/CAM	VLSI SYSTEM DESIGN	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
13-10-2020 TUESDAY	Advanced Power System Analysis	Advanced CAD	VLSI Technology	Data Structures and Algorithms	Data Structures and Algorithms
15-10-2020 THURSDAY	Advanced Power System Protection	Computer Aided Manufacturing	CMOS Analog Integrated Circuit Design	Database Internals	Software Development Methodologies
20-10-2020 TUESDAY	Modern Control Theory	Advanced FEM	CMOS Digital Integrated Circuit Design	Distributed Systems	Software Requirement and Estimation
<b>Core Elective-I</b>					
22-10-2020 THURSDAY	1. EHV AC Transmission 2. High Voltage Engineering 3. Advanced Digital Signal	1. Mechanical Behaviour of Materials 2. Stress analysis and	1. DSD 2. HSCOD 3. CPLD & FPGA	1. Network Security 2. Android Application Development	1. Cloud Computing 2. Database Internals 3. Component Based Software
<b>Core Elective-II</b>					
28-10-2020 WEDNESDAY	1. Power Quality 2. MP&MC 3. Distribution Automation	1. Automation in Manufacturing 2. CAPP 3. DMR & AMS	1. AVLSIDA EBS Design 3. Device Modeling	2. 1. Machine Learning 2. Parallel and Distributed Algorithms 3. SAPP	1. Big Data and Analytics 2. Web Mining 3. Object Oriented Modelling 4. Information Theory and
<b>Open Elective-I</b>					
31-10-2020 SATURDAY	1. OT 2. DCS 3. RES 4. HVDCT 5. APC 6. EBS	1. NMPDE 2. POM	1. Soft Computing Techniques 2. I&VP 3. software Defined Radio	JP	JP

NOTE: 1) ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

Dt:- 30-09-2020

  
**CONTROLLER OF EXAMINATIONS**

  
**CHIEF CONTROLLER OF EXAMINATIONS**

**JB INSTITUTE OF ENGINEERING AND TECHNOLOGY R18**

UGC AUTONOMOUS, EXAMINATION BRANCH

**TIME-TABLE OF I M.Tech. I-Sem Supplementary Examinations –R18-2019, 2018 BATCH – OCT 2020**

Time: 10:30 AM to 12:30 PM

Branch	EEE	ME	ECE	CSE
Course Date/Day	ELECTRICAL POWER SYSTEMS	CAD/CAM	VLSISD	COMPUTER SCIENCE & ENGINEERING
13-10-2020 TUESDAY	ADVANCED POWER SYSTEM ANALYSIS	ADVANCED CAD	DIGITAL SYSTEM DESIGN USING HDL	ADVANCED DATA STRUCTURES
15-10-2020 THURSDAY	FLEXIBLE AC TRANSMISSION SYSTEM	ADVANCED COMPUTER AIDED MANUFACTURING	DIGITAL CMOS INTEGRATED CIRCUIT DESIGN	CRYPTOGRAPHY AND NETWORK SECURITY
20-10-2020 TUESDAY	ENERGY AUDITING CONSERVATION AND MANAGEMENT	<b>RAPID PROTOTYPING TECHNOLOGIES/ MECHANICAL BEHAVIOUR MATERIALS</b>	DIGITAL SYSTEM DESIGN	MACHINE LEARNING
22-10-2020 THURSDAY	DISTRIBUTION AUTOMATION	AUTOMATION IN MANUFACTURING	VLSI TECHNOLOGY AND DESIGN	DISTRIBUTED SYSTEMS
28-10-2020 WEDNESDAY	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR
31-10-2020 SATURDAY	DISASTER MANAGEMENT	DISASTER MANAGEMENT	<b>PEDAGOGY STUDIES/ ENGLISH FOR RESEARCH PAPER WRITING</b>	PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS

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 UGC AUTONOMOUS YENKAPALLY, MOINABAD – 500 075, HYDERABAD EXAMINATION BRANCH  
**TIME-TABLE OF I M.Tech. II-Sem. Supply Examinations –R15-2017, 2016, 2015 BATCHES – OCT 2020**

Time: 10:30 AM to 12:30 PM

Branch	EEE	MECHANICAL	ECE	CSE	IT
Course Date/Day	ELECTRICAL POWER SYSTEMS	CAD/CAM	VLSI SYSTEM DESIGN	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
12-10-2020 MONDAY	Power System Dynamics	Design for Manufacturing And Assembly	Low Power VLSI Design	Network Programming	Software Architecture and Design
14-10-2020 WEDNESDAY	Flexible AC Transmission Systems (FACTS)	Flexible Manufacturing Systems	Design for Testability	Information Retrieval Systems	Software Process and Project Management
16-10-2020 FRIDAY	Power System Operation and Deregulation	Industrial Robotics	CMOS Mixed Signal Circuit Design	Internet Technologies and Services	Software Quality Assurance and Testing
<b>Core Elective-III</b>					
19-10-2020 MONDAY	1. Gas Insulated Systems(GIS) 2. Programmable Logic Controllers and their applications 3. High frequency magnetic components	1. Intelligent Manufacturing Systems 2. Special Manufacturing Process 3. Design Optimization	1. VLSI and DSP Architectures 2. Full custom Design 3. Hardware Description Language	1. Data Mining 2. Storage Area Networks 3. Semantic Web and Social Networks 4. Cyber Security	1. Scripting Languages 2. Information Retrieval Systems 3. Semantic Web and Social Networks 4. E-Commerce
<b>Core Elective-IV</b>					
21-10-2020 WEDNESDAY	1. Reactive Power Compensation and Management 2. Power System Reliability 3. Voltage Stability	1. Advanced Mechatronics 2. Design and Manufacturing of MEMS and Micro Systems 3. Fuzzy Logic and	1. Optimization Techniques in VLSI Design 2. System On Chip Architecture 3. Semiconductor Memory Design and	1. Big Data Analytics 2. Soft Computing 3. Software Process and Project Management 4. Distributed Computing	1. Software Security Engineering 2. Cyber Security 3. Information Security And Audit 4. Business Process Management
<b>Open Elective-II</b>					
27-10-2020 TUESDAY	1. Instrumentation & Control 2. Intelligent Control 3. Smart grid technologies 4. AI Techniques in Electrical Engineering 5. Reliability Engineering 6. Energy Auditing, Conservation & Management	1. Engineering Research and Methodology 2. Quality Engineering in Manufacturing	1. Scripting Languages 2. Coding Theory and Techniques 3. Adhoc Wireless Networks	COMPUTER FORENSICS	Database Internals

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Date:- 30-09-2020

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**J.B. INSTITUTE OF ENGINEERING & TECHNOLOGY  
UGC AUTONOMOUS**

**EXAM BRANCH – CIRCULAR**


**TIME-TABLE**

**II M.TECH – I Semester (R18) 2018 Batch - SUPPLEMENTARY  
Examinations, NOV. 2020**

**Time: 10.30 AM to 12.30 PM**

<b>Date, Day &amp; Branch</b>	<b>CAD/CAM</b>	<b>EPS</b>	<b>VLSISD</b>	<b>CSE</b>
02-11-2020 MONDAY	PROGRAM ELECTIVE-V Advanced Industrial Robotics	PROGRAM ELECTIVE-V Reliability Engineering	PROGRAM ELECTIVE-V Advanced Computer Architecture	PROGRAM ELECTIVE-V Blockchain Technology
04-11-2020 WEDNESDAY	OPEN ELECTIVE Industrial Safety	OPEN ELECTIVE Renewable Energy Systems	OPEN ELECTIVE Digital Signal Processors and Architectures	OPEN ELECTIVE Python Programming

**NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the Undersigned.**

  
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CHIEF CONTROLLER OF EXAMINATIONS

**Dt: 30.09.2020**

Copy to: Principal, All ACEs, MBA, MCA (HOD'S), Concerned Seat Clerk &  
NOTICE BOARD