

Profile of the Faculty Members



- 1) Name :: K.V.Ranga Rao
- 2) Date of Birth :: 13-12-1969
- 3) Educational Qualifications :: B.Tech(CSE), M.S.,M.Tech, (Ph.D)
- 4) Work Experience ::
 - a)Teaching :: 19 years
 - b)Research ::-
 - c)Industry ::-
 - d)Others ::-
- 5) Area of Specialization :: Computer Science Engg
- 6) Subjects teaching at UG Level: Almost all the subjects
Graduate Level : DAA,ANN,SA
- 7) Research Guidance ::
 - No.of papers published in Master's ::- 4
 - National Journals ::-
 - International Journals ::
 - Conferences ::- 1
 - Work Shops: 6
- 8) Projects carried out ::-
- 9) Patents ::-
- 10) Technology Transfer ::- 1
- 11) Research Publications :: 7

National Conferences:

- I Attended a national conference on Soft Computing and its applications and presented a paper on "Course and Exam Scheduling based Genetic Algorithm at ANITS, Vishakapatnam during 21st, 22nd December 2007.

International Conferences or International Journals:

- I. "Multi threading to Core spilling: A Survey on modern trends in Multi core Processors" IBM Research India Quarterly March 2009.
- II. Attended an International Conference on MEMS & Optoelectronicss Technologies(ICMOT-2010) during 22nd -23rd January 2010 organized by International Accreditation Council of Quality Education & Research -New Delhi conducted at Swarnandhra College Of Engineering & Tenchnolgy – Narsapuram and presented a research paper "Multithreading to Corespilling : A survey on Modern trends in Multi Core Processors".

- III. [A Survey of Recent Trends in Improvement of Multi Core Processors Performance” in International Journal of Engineering Research and Industrial Applications \(IJERIA\) ISSN 0974-1518 Volume 3, No. II \(May 2010\) pages 237-258.](#)
- IV. [“The Multicore bottle neck” in International Journal of Multidisciplinary Research and Advances in Engineering \(IJMRAE\) ISSN 0975-7074 Volume 2, No. II July 2010 pages 149-159.](#)
- V. [Presented a paper titled “Factors affecting Multicore Processor Performance” in “Annual International Conference of Computer Science Education : Innovtion & Technology \(CSEIT 2010\)” to be held on 6th- 7thDecember 2010, at phuket Thailand organized by Fifteen International Universities across the Globe and Global Science & Technology Forum \(GSTF\)](#)
- VI. Presented a paper titled “FORMAL MODELING OF DYNAMIC MOBILE DEVICE GRID” at “2nd International Vaagdevi Conference on Information Technology for Real World Problems(VCON 10)” being organized by Hyderabad IEEE society, to be held on 9th- 11th December 2010, at Vagdhevi College of Engineering & Technology – Warangal.

12) No.of Books published ::-